

Dell PS Series Configuration Guide

Abstract

This configuration guide provides technical guidance for designing and implementing Dell™ PS Series storage solutions in iSCSI SAN environments.

November 2019

Revisions

Date	Description	
November 2019	vVols branding update	
May 2018	Minor updates	
December 2016	Minor updates	
January 2016	Small text updates and additions of PS4210, PS6210, and PS6610 array	
August 2014	Table 31: 14-drive RAID policy added	
May 2014	Minor updates for PS6210. Added information for LLDP	
February 2014	Section 9.7, Vertical port failover behavior in PS6210 controllers.	
	New information in 8.1.2 on how to ensure a secure network environment.	
	New information for controller type 15 in Table 6.	
October 2013	Added Purpose, Policies and Limitations, General Statement, and Policies for qualifying systems.	
	Eliminated redundant information that is available in other documents and provided links to that documentation.	
	Added high-availability chapter.	
June 2013	Small text updates	
March 2013	Added section 13, Data Center Bridging.	
	Added volume limits for synchronous replication in Table 4.	
	Modified section 8.1.2 and section 8.2.	
	Changed all references in tables in section 7 to read PS41x0 and PS61x0.	
	Updated appendix B, PS Series upgrade paths.	
	Updated the related publications.	

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Abstract

This configuration guide provides technical guidance for designing and implementing Dell™ PS Series storage solutions in iSCSI SAN environments.

This guide includes the following:

- Details on the capacity and capabilities of the PS Series family and the FS Series family of NAS appliances
- iSCSI SAN design guidelines for optimizing redundancy, availability, and performance of PS Series storage arrays

Dell PS Series arrays provide a storage solution that delivers the benefits of consolidated networked storage in a self-managing, iSCSI storage area network (SAN) that is affordable and easy to use regardless of scale. By eliminating complex tasks and enabling fast and flexible storage provisioning, these solutions dramatically reduce the costs of storage acquisition and ongoing operations.

To be able to leverage the advanced features provided by a PS Series array, a robust, standards-compliant iSCSI SAN infrastructure must be created. While almost any industry standard, enterprise class Ethernet switch can be used to connect hosts to the PS Series-based virtual storage volumes, some switches, network interface controllers (NICs), iSCSI Host Bus Adapters (HBAs), and iSCSI initiators can directly impact the overall performance of the SAN storage solution. The *Dell PS Series Configuration Guide* is provided as an aid to help storage administrators determine how to best build an iSCSI infrastructure for use within a PS Series SAN solution. This document focuses on network configuration, host integration, and other topics that help to ensure a smooth deployment with optimum PS Series SAN and FS Series NAS appliance performance.

Unless otherwise stated, recommendations in this document are applicable to both file and block environments.

This document should be used strictly as a guide in planning a PS Series storage solution. It should not be considered a statement of support for any specific configuration. Actual viability of any configuration will depend on the capabilities of the individual components (for example, switches, network interface controllers, initiators) that make up the SAN infrastructure.

1 Purpose

The goal of this guide is to provide a single reference for technical information, links to other product and technical information, and recommended PS Series SAN design methodologies.

This document is for informational purposes only and is offered as is.

This document is not intended to be used as:

- A document for statement of support for any specific configuration
- · Approval for specific configurations or reference architectures

1.1 Dell EMC statement of support

The scope of products available for Ethernet networking is too broad to allow Dell EMC to certify every device for use within a PS Series infrastructure. The goal is not to restrict which infrastructure components you can integrate with the PS Series SAN, but to provide a clear statement of how Dell EMC supports devices that have not been tested or sold by Dell EMC.

1.2 General statement

In general, Dell EMC allows any infrastructure component to be used within a PS Series SAN solution, assuming it meets the minimum standards as described in this document required to be used within a PS Series SAN infrastructure. Support resolves customer issues with the SAN solution until it has been determined that the infrastructure component is identified as causing the issue. If the identified component is not included in a Dell EMC support agreement or Dell EMC does not have a predefined support relationship (Cooperative Support Agreement), you may be required to directly contact that component provider for further support. Policies and procedures for support of PS Series products are explained in the Dell EMC support statement found at the link in the "Policies and limitations" section below.

1.3 Audience

This configuration guide is intended for storage administrators, SAN/NAS system designers, storage consultants, or anyone who is considering purchasing or has purchased PS Series storage or FS Series array appliances for use in a production SAN. It is assumed that all readers have experience in designing and/or administering a shared storage solution. Also, there are some assumptions made in terms of familiarity with all current Ethernet standards as defined by the Institute of Electrical and Electronic Engineers (IEEE) as well as all TCP/IP and iSCSI standards as defined by the Internet Engineering Task Force (IETF).

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2 Policies and limitations

- This document is being provided for informational purposes only and may change at any time
- This version supersedes and replaces all previous versions
- The information included in this document is intended as a guide for planning the configuration of systems for PS Series infrastructure and networking environments
- It is not intended to be the sole resource for system configurations
- For more information or questions not answered in this document, see relevant product documentation, contact your sales representative, or contact your customer service and/or technical support representative

2.1 Policies for qualifying systems

A list of validated hardware and software components, as well as policies and procedures for support of PS Series products, are provided in the <u>Dell EMC Storage Compatibility Matrix</u> (DSCM).

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3 Related publications

The following locations provide additional background and technical details supporting configuration of PS Series SANs.

PS Series product documentation at https://eqlsupport.dell.com/support/download.aspx requires a support account to log in.

To access the administration guides and other product documentation, follow this link and follow these steps:

- 1. Select **PS Series Firmware**.
- 2. Select the current version for the Download Page link.
- 3. Select the firmware version ZIP file to begin download.
- 4. Select your guide under **Documentation**.

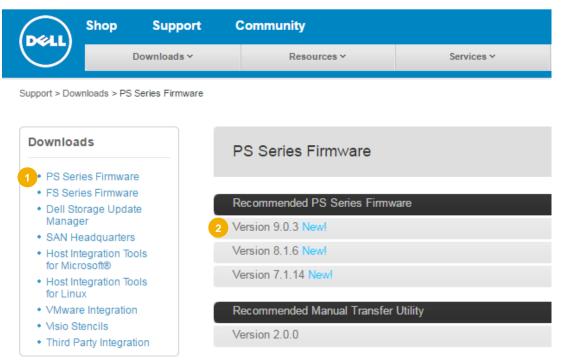


Figure 1 PS Series Firmware, support download page

3 v9.0.3 Firmware (zip) MIBS MIBS Differences Host Scripting Tools - Windows Host Scripting Tools - Linux Important note: There are now two separate kits available within the v9.0.x Firmware .zip file: 32-bit and 64-bit. After you download the .zip file, when you install the update, the wizard will ask you to select the kit to install. Select the 32-bit kit for the following array models: PS4000, PS-M4110 PS6000, PS6010 PS6500, PS6510 Select the 64-bit kit for the following array models: PS4100, PS4110 PS4210 PS6100, PS6110 PS6210, PS6610 See the PS Series release notes for additional information. Date Released October 2016 PS Series Storage Arrays Release Notes iSCSI Initiator and OS Considerations for HA Group Mgr Admin Guide Master Glossarv Updating FW for PS FS Online Help

Figure 2 PS Series Firmware download and support documents

- <u>Dell EMC Storage Compatibility Matrix</u>, including recommended switches and supported iSCSI initiators
- PS Series technical documents and videos
- Rapid EqualLogic Configuration Portal
- Switch Configuration Guides

PS Series Firmware - v9.0.3

- PS Series Hardware Documentation (requires support login)
- Dell EqualLogic Group Manager Administrator's Manual (requires support login)
- VMware ESXi NIC Optimization and Best Practices with EqualLogic SAN
- Best Practices for Configuring DCB with Windows Server and EqualLogic Arrays
- Red Hat Enterprise Linux 6.3 NIC Optimization and Best Practices with EqualLogic SANs
- Configuring a Dell EqualLogic SAN Infrastructure with Virtual Link Trunking (VLT)
- Best Practices for Securing a Dell Storage PS Series SAN

4 PS Series storage arrays

PS Series storage SANs provide a peer storage architecture comprised of one or more independent arrays. Each array contains its own controllers, cache, storage, and interface ports. Grouped together, they can create one or more single instance storage pools that are based on the IETF iSCSI standard. The capabilities of the PS Series solution are provided through a unique set of software and hardware capabilities that, when used cooperatively, can provide a full featured SAN solution. The following sections provide additional insight into specifications and functions of PS Series arrays.

4.1 Array models

The array models available prior to introduction of the PS4100/PS6100 family are shown in Table 1.

Table 1 Array models prior to PS4100/PS6100

Array model	Drive type	Number of drives
PS4000E	SATA	16
PS4000X, PS4000XV	SAS	16
PS5000E	SATA	16
PS5000X, PS5000XV	SAS	16
PS5500E	SATA	48
PS6000E	SATA	16
PS6000X, PS6000XV	SAS	16
PS6000S, PS6010S	SSD	16
PS6010E	SATA	16
PS6010X, PS6010XV	SAS	16
PS6000XVS, PS6010XVS	SAS / SSD	8/8
PS6500E	SATA	48
PS6510E	SATA	48
PS6510X	SAS	48
PS6500ES	SAS / SSD	41 SAS + 7 SSD
PS6510ES	SAS / SSD	41 SAS + 7 SSD

Starting with the introduction of the PS4100 and PS6100 family of arrays, configurations using 2.5" and 3.5" disks are available.

Table 2 PS4100/PS6100/PS6210 array models

Array model	Drive type	Number of drives
PS4100E	3.5" SAS 7.2K RPM	12
PS4100X	2.5" SAS 10K RPM	24
PS4100XV	2.5" SAS 15K RPM	24
PS4100XV	3.5" SAS 15K RPM	12
PS6100E	3.5" SAS 7.2K RPM	24
PS6100X	2.5" SAS 10K RPM	24
PS6100XV	2.5" SAS 15K RPM	24
PS6100XV	3.5" SAS 15K RPM	24
PS6100S	SSD	12 or 24
PS6100XS	SSD + SAS 10K RPM	7 SSD + 17 SAS
PS4110E	3.5" SAS 7.2K RPM	12
PS4110X	2.5" SAS 10K RPM	24
PS4110XV	2.5" SAS 15K RPM	24
PS4110XV	3.5" SAS 15K RPM	12
PS6110E/PS6210E	3.5" SAS 7.2K RPM	24
PS6110X/PSS6210X	2.5" SAS 10K RPM	24
PS6110XV/PS6210XV	2.5" SAS 15K RPM	24
PS6110XV/PS6210XV	3.5" SAS 15K RPM	24
PS6110S/PS6210S	SSD	12 (PS6110S only) or 24
PS6110XS/PS6210XS	SSD + SAS 10K RPM	7 SSD + 17 SAS
PS-M4110E	7.2K RPM NL-SAS 2.5"	14
PS-M4110X	10K RPM SAS 2.5"	14
PS-M4110XV	15K RPM SAS 2.5"	14
PS-M4110XS	SSD + 10K RPM 2.5" SAS	5 SSD + 9 SAS

Table 3 PS4210, PS6210, and PS6610 array models

Array model	Drive type	Number of drives
PS4210E	3.5" 7.2K NL-SAS	12
PS4210X	2.5" 10K SAS	24
PS4210XV	2.5" 15K SAS	24
PS4210XS	2.5" SSDs + 2.5" 10K SAS	24
PS6210E	3.5" 7.2K NL-SAS	24
PS6210X	2.5" 10K SAS	24
PS6210XV	2.5" 15K SAS	24
PS6210S	2.5" SSDs	24
PS6210XS	2.5" SSDs + 2.5" 10K SAS	24
PS6610E	3.5" 7.2K NL-SAS	42 x 2TB, 4TB, 6TB, or 8TB; or 84 x 4TB, 6TB, or 8TB
PS6610X	2.5" 10K SAS	84 x 600GB; 42 or 84 x 900GB, 1.2TB, or 2.4TB
PS6610ES	2.5" SSDs + 3.5" 7.2K NL-SAS	84

4.2 PS Series supported configuration limits

Dell PS6xxx Series arrays provide the full range of features and capabilities available with the PS Series storage products. Dell PS4xxx Series arrays provide a subset of features and capabilities, targeted at remote office and small-to-medium business storage deployments. The supported configuration limits for a PS Series group are provided in Table 4.

Table 4 Supported configuration limits

Configuration	PS4000, PS4100, PS4110, PS4210, and PS-M4110 groups only ^(a)	All other groups ^(b)
Volumes and replica sets per group	512 ^(h)	1024
Volume size ^(c)	15 TB	15 TB
Volumes that have replication enabled (outbound) ^(d)	32	256
Snapshots + replicas + VMware [®] vSphere [®] Virtual Volumes [™] (vVols) per group ^(j)	2048	10,000
Snapshots per volume	128	512
Replicas per volume	128	512
Volumes that have Synchronous Replication Enabled	4	32 ^(k)
Schedules (snapshot or replication) per volume or volume collection	64	64

Configuration	PS4000, PS4100, PS4110, PS4210, and PS-M4110 groups only ^(a)	All other groups ^(b)
Persistent Reservation registrants per volume	512 per pool	1024 per pool
	1024 per group with 2 pools	4096 per group with 4 pools
Replication partners per group	16	16
Replication partners per volume	1	1
Members per group	2	16 (a)
		8 if using vVols
Members per pool	2	8
Pools per group	2	4
Volumes per collection	8	8
Collections per group (snapshot and replication)	100	100
Volume connections (each time an iSCSI initiator connects to a volume counts as	512 per pool ^(g)	1024 per pool ^(h)
a connection)(e)(f)	1024 per group with 2 pools	4096 per group with 4 pools
Basic access points per volume and its snapshots	16	16
Simultaneous management sessions (any combination of GUI, telnet, or scripting sessions)	7	7
Thin provisioning limits (minimum allocation)	10% of volume size	10% of volume size
Administrator accounts per group	100	100
Total combined folders and volume tags per group	1024	1024
vVols + volumes per group	512	1024
Bound vVols + online volumes + online snapshots (max 100) per group	512	1024
vVol metadata	64K	64K
Number of online snapshots	100	100
Recommended minimum free space per pool	5% of total pool capacity or 100GB per pool member, whichever is less	5% of total pool capacity or 100GB per pool member, whichever is less
Access policy groups	128	128
Access policies	512	512
Access points	1024	1024
Access point IP address	2048	2048

Configuration	PS4000, PS4100, PS4110, PS4210, and PS-M4110 groups only ^(a)	All other groups ^(b)
Associations	4096	4096
SCSI Power Fencing ^{(l)(m)}	Up to 16 nodes	Up to 16 nodes
IPsec policies	256	256
IPsec security parameters	256	256
IPsec certificates ^(m)	10	10

- a. A group can contain a maximum of two PS4000, PS4100, PS4110, PS4210, and/or PS-M4110 arrays.
- b. Includes groups of mixed array types and all other group types except groups containing only PS40x0, PS41x0, PS42x0, and PS-M4110 arrays. When a group contains a mix of PS40x0, PS41x0, PS42x0, and PS-M4110 arrays and other array models, the higher limits prevail.
- c. Practical maximum volume size is operating system-specific. A PS Series group can create and present volumes up to 15 TB.
- d. Up to 16 simultaneous replication operations can take place.
- e. To avoid service outages or possible loss of connectivity during failovers, Dell EMC recommends increasing initiator timeouts for groups with more than 512 connections. See the iSCSI Initiator and Operating Systems Considerations document for more information.
- f. Inbound replication connections count towards the total number of connections.
- g. With firmware version 5.1, maximum number of volumes per group and maximum number of connections per pool increased from 256 to 512.
- h. Up to 1024 connections per pool are supported for all groups except groups containing only PS40x0, PS41x0, PS42x0, and PS-M4110 arrays. This provides increased scalability in virtualized environments. These connections may be distributed across a maximum of 512 volumes.
- Volumes automatically created and managed by the NAS Service inside the NAS Reserve pool used by an FS Series appliance are fully allocated at creation and cannot be thin provisioned.
 Once those volumes are created (when the NAS Reserve is created they cannot be shrunk).
- j. This value includes vVols, vVol snapshots, and vVol-lined clones.
- k. When mixing PS6XXX and PS4XXX arrays, SyncRep remains at 4 volumes if any PS4XXX members are in the group.
- I. SCSI fencing is not supported for Red Hat® Enterprise Linux®. See the iSCSI Initiator and Operating System Considerations document for more information.
- m. This value includes one local and one root CA certificate; the rest are intermediate CA certificates.

4.3 Controller types in all models prior to PS4100/PS6100

Array controllers can be identified and differentiated by the controller "type" designation. Each controller type will have a different colored label to help quickly identify the controller type. Table 5 lists each Dell PS Series controller along with some characteristics about each.

Table 5 Array controller types of all models prior to PS4100/PS6100

Controller type	Faceplate	Network interfaces	Storage type	Notes
Type 3 SAS	EQUALLOGIC	3 x 1GbaseT	SAS	PS3000 – PS5000
Type 3 SATA	PRINCEION DON'TON'S STREETING / DON'TON'S ST		SATA	1GB cache
				Cannot mix Type 3 SAS with Type 3 SATA
Type 4	ACT 1000 100	3 x 1GbaseT	SAS	PS3000 – PS5000
	COMPANY MICROSOFT			1GB cache
				Cannot mix Type 3 SAS with Type 4 controller
Type 5	ACT O SMALPCHTO CHOMBT J CHOMB	3 x 1GbaseT	SATA	PS3000 – PS5000
	MAIN COMPANY, MODIFIES			1GB cache
				Cannot mix Type 3 SAS with Type 5 controller
Type 6	ACT O SHARKSET STREET S	3 x 1GbaseT	SATA	PS5500 only
	CONTROL MODULE 6 DÉLL			2GB cache
Type 7	CHEMITA ENGINEER CHEMITA CHEMITA CHEMITATE CHE	4 x 1GbaseT	SAS	PS6000 – PS6500
	CONTROL MODULE?		SATA	2GB cache
			SSD	Cannot mix SAS and SATA drives in one array
Type 8		2 x1GbaseT	SAS	PS4000 only
	CONTROL MODULE 8 DOLL EQUALLOGIC*	1 x 10/100Mb mgmt.	SATA	2GB cache
				Cannot mix SAS and SATA drives in same array
Type 9	MAA. PORTS MAANDEST ENGINEETS ENGINEETS	2 x 1GbaseT	SAS	2 nd generation PS4000
	CONTROL MODULE 9 DOLL EQUALLOGIC*	1 x 10/100Mb mgmt.	SATA	2GB cache
				Cannot mix SAS and SATA drives in same array

Controller type	Faceplate	Network interfaces	Storage type	Notes
Type 10	MANAGER AND	2 x 10GB SFP+	SAS	10Gb Ethernet
	COMMON MOCKET IS DOLL EQUALLOGIC*	1 x 10/100Mb mgmt.	SATA	PS6010 – PS6510
			SSD	2GB cache

4.4 Controller types in PS4100/PS6100 and later models

The new controller types available in the PS4100/PS6100 and later model arrays became available starting in August 2011. Table 6 lists each Dell PS Series controller along with some characteristics.

Table 6 PS4100/PS6100/PS6210 controller types

Controller type	Faceplate	Network interfaces	Storage type	Notes
Type 11	CONTRACTOR FRANCE FRANC	4 x 1GbaseT	SAS	PS6100 only
		1 x 10/100Mb mgmt.	NL-SAS SSD	4GB cache
				Supports vertical port failover
				Cache to Flash memory de-staging
Type 12	CONTRACTOR STATE OF THE STATE O	2 x 1GbaseT	SAS	PS4100 only
		1 x 10/100Mb mgmt.	NL-SAS	4GB cache
				Supports vertical port failover
				Cache to Flash memory de-staging
Type 14	CONTINUE NOTICE AND ADDRESS OF THE PARTY OF	1 x 10GbE SFP+	SAS	PS6110 only
		1 x 10GbaseT	NL-SAS	4GB cache
		10/100Mb mgmt	SSD	Support for vertical port failover
				Cache to Flash memory de-staging
				SFP+ OR 10GBaseT used at any given time

Controller type	Faceplate	Network interfaces	Storage type	Notes
Type 15	CONTRECE SCORES 1 CONTRECT 1 CONT	2 x 10GbE SFP+ 2 x 10GbaseT 10/100Mb mgmt	SAS NL-SAS SSD	16GB memory per controller, 10GBaseT ports –can autonegotiate to 1G. Cache to Flash (C2F) destaging. New battery backup for cache. Hot swappable, Active-hot stand-by redundancy.
Type 17	CONTON MONATO F TOWART 1 SEAL TO THE PARTY OF THE PARTY O	1 x 10GbE SFP+ 1 x 10GbaseT 1 x 10/100Mb mgmt.	SAS NL-SAS	PS4110 only 4GB cache Supports vertical port failover Cache to Flash memory de-staging SFP+ OR 10GBaseT used at any given time
Type 18		2 x 10GBASE-T 2 x 10GbE SFP+ 1 x 10/100Mb mgmt.	SAS NL-SAS SSD	PS6610
Type 19		2 x 10GBASE-T 2 x 10GbE SFP+ 1 x 10/100Mb mgmt.	SAS NL-SAS SSD	PS4210

Note: Controller types 1 through 6 will not accept PS Firmware version 8. Controller types 1 through 3 will not accept PS Firmware version 7.

4.5 Array model PS-M4110

4.5.1 Controller type in PS-M4110 model

The PS-M4110 controller is designed based on a modified version of the PS4100 controller. Host and SAS cards are combined to form a single unit fixed I/O module, connecting to the M1000e chassis infrastructure.

Controller type	Storage blade image	Network interfaces	Storage type	Notes
Type 13		2 x 10Gb ports, (one per controller), connected through the backplane (no label) Each of the two ports has an active link and a standby link to the fabric switches in the backplane of the M1000e chassis.	SAS NL-SAS	Dual, hot-pluggable 10GbE controllers. 2GB of memory per controller, Cache-to-Flash design. 1 x dedicated management port – accessible by CMC. 14 x 2.5" 6Gb/s SAS disk drives.

4.5.2 Configuration options

The PS-M4110 has four basic configuration options. It can be configured on Fabric A or Fabric B, and each Fabric Configuration can use a 10Gb KR switch or a 10Gb KR Pass-Through Module (PTM). Figure 3 depicts a basic configuration using Dell EMC Networking MXL switches, however any supported switches can be used in this configuration.

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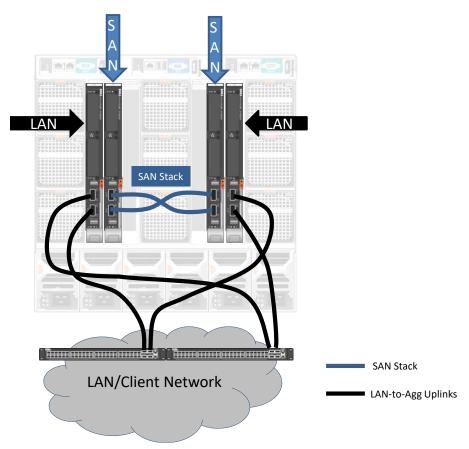


Figure 3 Basic PS-M4110 configuration for data center-in-a-box

4.5.3 Networking considerations and guidelines

Supported M-Series I/O modules:

- 10G KR is the only supported I/O Module (IOM).
- Switches: The list of supported switches can be found in the Dell EMC Storage Compatibility Matrix.

The following are basic networking recommendations for implementing the PS-M4110 storage blade.

- IOMs must be Interconnected.
- External switches can be used to provide interconnection if rack mounted arrays are needed.
- Must use "Single Fabric" I/O module Placement; supported fabrics are "A" and "B" only.
- Fabric A is not supported on older M1000e systems (Mid-plane v1.0). The version should be physically marked on the chassis or checked using the CMC command. Upgrading is allowed but generally not recommended because of the required downtime and required professional services.
- It is recommended that PS-M4110 controller be placed into their own pool (not mixed with rack-mount arrays).

For a more in-depth understanding of PS-M4110 Storage Blade, see the following white papers:

- Dell EqualLogic PS-M4110 Blade Array Technical Article
- Dell PowerEdge M1000e Blade and EqualLogic PS Series 10 GbE SAN Design Best Practices
- Dell PowerEdge M1000e Blade Enclosure and Dell PS Series SAN Design Best Practices Using Dell S-Series and M-Series Networking

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5 Controller firmware

5.1 About member firmware

Each control module in a group member must be running the same version of the PS Series Firmware. Firmware is stored on a compact flash card or a microSD card on each control module.

Dell EMC recommends the following:

- Always run the latest firmware to take advantage of new features and fixes.
- All group members must run the same firmware version. If you are adding a new array to a group, update the group to the latest firmware before adding the new member.
- Dell EMC does not support groups running with mixed-firmware versions, except when you are in the
 process of upgrading the firmware of the entire group, and then only for as long as it takes to do the
 upgrade.
- You can upgrade member firmware to a higher version or downgrade member firmware to a lower version. In some firmware releases, firmware downgrades are not allowed. See the Dell PS Series Storage Arrays Release Notes for version-specific information about the firmware running on your group member(s).
- If you are running older firmware versions, or updated across multiple releases, you might have to update to one or more interim versions before updating to the latest firmware. For example, to update a group from version 5.2.9 to version 7.1.6, you must first update the version 6.0.5, then to version 7.1.6.
- For instructions on how to update the firmware, see the document *Updating Firmware for Dell EqualLogic PS Series Storage Arrays and FS Series Appliances*, available at the Dell PS Series customer support site (requires support login):
 https://eglsupport.dell.com/support/download_file.aspx?id=2930
- For guided update management of Dell EMC storage products, the Dell Storage Update Manager simplifies the update process. More information is available at the Dell PS Series customer support site (requires support login): https://eqlsupport.dell.com/support/download.aspx?id=6442455985

Each PS Series array runs a core operating system in firmware that provides all of the PS Series features and functionality. The firmware version is defined using a version number and will be updated from time to time as new features are added or for general maintenance improvements.

The firmware version number takes the following form: X.Y.Z.

- **X** is used to identify the "major" release number. This changes when there are very significant changes in the features and functionality.
- Y is the "minor" release number. This changes as new minor features or improvements in existing features are made for a given major release.
- **Z** denotes maintenance releases. This changes whenever a maintenance release level is released to resolve any identified technical or usability issues.

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5.2 Firmware upgrade considerations

Before beginning a firmware upgrade process, review the following documentation. These documents are available from the Dell PS Series Support site at

<u>https://eqlsupport.dell.com/support/download.aspx?id=6442456455</u> (Support ID required for login access). These documents include:

- Release Notes and Fix List for the firmware version to which you are upgrading
- Release Notes for any FS Series appliances you are operating
- Updating Firmware for Dell EqualLogic PS Series Storage Arrays and FS Series Appliances, available at: https://eqlsupport.dell.com/support/download_file.aspx?id=2930

6 RAID policies

Each array in a PS Series array group is configured with a single RAID policy. Arrays (or group members) within the same storage pool that have the same RAID policy cooperatively work to host volumes by distributing those volumes over multiple arrays. Two things that are defined by the RAID policy are:

- RAID level
- Hot-spare configuration

Each array implements a default RAID policy that includes a hot-spare.

The following report contains information about performance characteristics of the RAID policies under normal and failure conditions and help when choosing the RAID policy that meets the requirements for specific applications and environments. For a complete understanding of supported RAID policies for PS Series Group Members, see the report at:

http://en.community.dell.com/dell-groups/dtcmedia/m/mediagallery/19861480

D&LLEMC

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7 PS Series capacity

7.1 RAID 6 drive layouts and total reported usable storage

RAID 6 (striped set with dual distributed parity) combines N disks in an arrangement where each stripe consists of N-2 disks capacity for data blocks and two disks capacity for parity blocks. Each parity block generates parity using a different view of the data blocks depending on the RAID 6 implementation. RAID 6 can tolerate up to two drive failures per RAID stripe set at the same time without data loss. RAID 6 is not recommended for workloads consisting mainly of random writes. Using a RAID 6 policy, Table 7 shows the drive layouts that are enforced based on the number of drives in each array/hot spare configuration, and the total usable storage available for each model.

Table 7	RAID 6 drive la	vouts and total storage	available with hot spares	(in GB)

Disk drives	Hot spare	No hot spare
6	5 data/parity + 1 hot spare	6 data/parity
7	6 data/parity + 1 hot spare	7 data/parity
8	7 data/parity + 1 hot spare	8 data/parity
12 ^(f)	11 data/parity + 1 hot spare	12 data/parity
14	13 data/parity + 1 hot spare	14 data/parity
16	15 data/parity + 1 hot spare	16 data/parity
8 + 8 ^(c)	15 data/parity (d) + 1 hot spare	16 data/parity
24 ^{(a)(f)}	23 data/parity + 1 hot spare	24 data/parity
7 + 17 ^(e)	23 data/parity + 1 hot spare	24 data/parity
42 ^(a)	40 data/parity + 2 hot spare	N/A ^(b)
48 ^(a)	47 data/parity + 1 hot spare	N/A ^(b)
84 ^(a)	82 data/parity + 2 hot spare	N/A ^(b)

- a. 24, 42, 48, and 84 drive arrays implement multiple RAID 6 sets within a single chassis.
- b. 42, 48, and 84 drive arrays cannot implement a no hot-spare RAID policy.
- c. PS60x0 XVS with 8 x 100GB SSD and 8 x 450GB SAS.
- d. One 450GB hot spare, 2 x 450 GB parity and 2 x 100GB SSD parity. In the event of a drive failure (SSD or SAS), the RAID set is reconstructed using the 450GB SAS hot spare.
- e. One 600GB hot spare, 2 x 600 GB parity and 2 x 400 GB SSD parity. In the event of a drive failure (SSD or SAS), the RAID set is reconstructed using the 600 GB SAS hot spare.
- f. PS41x0/PS61x0 and PS4210/PS6210 models only.

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Table 8 Total reported usable storage when using hot spares: All models prior to PS4100/PS6100

Drive qty.	50 ^(a)	100 ^(a)	74	146	250	300	400	450	500	600	750	1000	2000	3000
7 ^(b)	-	-	-	-	888	-	-	-	-	-	-	-	-	-
8(c)	-	-	-	-	1111	-	-	-	-	-	-	-	-	-
14 ^(d)	-	-	660	-	2224	-	3566	-	4460	-	6695	-	-	-
16	577	1155	-	1696	2895	3475	4638	5220	5800	6964	8708	11613	23633	35717
8 + 8 ^(e)	-	-	-	-	-	-	-	2539	-	-	-	-	-	-
48	-	-	-	-	-	-	-	-	18062	21677	-	36136	69260	107151

- a. Solid-state disk.
- b. Legacy PS50E
- c. PS4000E and PS6000E only
- d. Legacy PS70E, PS100E, PS100X, PS200E, PS300E, and PS400E
- e. PS60x0 XVS with 8 x100GB SSD and 8 x450GB SAS
- f. PS6100S (SSD) only
- g. PS6100XS only
- h. PSM4110XS only

Table 9 Total reported usable storage when using hot spares: PS41x0/PS61x0

Drive qty.	200 ^(f)	300	400 ^(f)	500	600	900	1000	1800	2000	2400	3000
6	-	-	-	1331	1597	-	2672	-	5457	-	8253
12	1658	2406	3328 ^(f)	4014	4812	7229	8038	-	16363	-	24719
14	-	2938	-	4906	5890, 4286 ^(h)	8841	9824	17682 ^(h)	-	23576 ^(h)	-
24	3512	5084	7034 ^(f)	8478	10178	15278	16947	-	34550	-	52202
7 + 17	-	-	-	-	9318 ^(g)	-	-	-	-	-	-

- a. Solid-state disk
- b. Legacy PS50E
- c. PS4000E and PS6000E only
- d. Legacy PS70E, PS100E, PS100X, PS200E, PS300E, and PS400E
- e. PS60x0 XVS with 8 x100GB SSD and 8 x450GB SAS
- f. PS6100S (SSD) only
- g. PS6100XS only
- h. PSM4110XS only

Table 10 Total reported usable storage when using hot spares: PS4210

Drive qty.	300	400	600	900	1200	2000	2400	3000	4000	6000
7 (SSD)	-	1846 ^(d)	-	-	-	-	-	-	-	-
17 (SAS)	-	-	7753 ^(d)	-	-	-	-	-	-	-
12	-	-	-	-	-	17012 ^(a)	-	25519 ^(a)	34025 ^(a)	51038 ^(a)
24	5261 ^(b)	-	10522	15783 ^(c)	21044 ^(c)	-	40627 ^(c)	-	-	-

- a. 4210E only
- b. 4210XV only
- c. 4210X only
- d. 4210XS only (RAID 6 Accelerated is the only option available for hybrid (mix of HDD and SSD) arrays)

Table 11 Total reported usable storage when using hot spares: PS6210

Drive qty.	800	900	1200	1600	1800	2400	3000	4000	6000	8000
7 (SSD)	3692 ^(f)	-	-	-	-	-	-	-	-	-
17 (SAS)	-	-	15506 ^(f)	-	-	-	-	-	-	-
24	14076 ^(e)	15278 ^(c)	20336 ^(c)	28152 ^(e)	30560 ^(a)	40627 ^(c)	52204 ^(b)	69857 ^(b)	107747 ^(b)	143662 ^(b)
		15280 ^(a)	18614 ^(a)			37288 ^(a)				

- a. SED
- b. 6210E only
- c. 6210X only
- d. 6210XV only
- e. 6210S only
- f. 6210XS only (RAID 6 Accelerated is the only option available for hybrid (mix of HDD and SSD) arrays)

Table 12 Total reported usable storage when using hot spares: PS6610

Drive qty.	400	600	800	900	1200	2000	2400	4000	6000	8000
14 (SSD)	4061 ^(c)	-	8122 ^(c)	-	-	-	-	-	-	-
70 (NL-SAS)	-	-	-	-	-	111527 ^(c)	-	223055 ^(c)	-	-
42	-	-	-	28243 ^(b)	37658 ^(b)	64270 ^(a)	37658 ^(b)	128540 ^(a)	192811 ^(a)	257081
84	-	38766 ^(b)	-	58149 ^(b)	77532 ^(b)	-	155064 ^(b)	264642 ^(a)	396963 ^(a)	529284

- a. 6610E only
- b. 6610X only
- c. 6610ES only (RAID 6 Accelerated is the only option available for hybrid (mix of HDD and SSD) arrays.)

7.2 RAID 10 drive layouts and total reported usable storage

Using a RAID 10 policy, Table 13 shows the drive layouts that are enforced based on the number of drives in each array/hot spare configuration, and the total usable storage available for each model.

RAID 10 (mirrored sets in a striped set) combines two high performance RAID types: RAID 0 and RAID 1. A RAID 10 is created by first building a series of two disk RAID 1 mirrored sets, and then distributing data over those mirrors. RAID 10 implementations can tolerate one drive failure per mirrored pair.

Table 13 RAID 10 drive layouts and total reported usable storage available with hot spares (in GB)

Disk drives	Hot spare	No hot spare
6	4 data (2 mirrors) + 2 hot spares	6 data (3 mirrors)
7	6 data (3 mirrors) + 1 hot spare	Not supported
8	6 data (3 mirrors) + 2 hot spares	8 data (4 mirrors)
12 ^(c)	10 data (5 mirrors) + 2 hot spares	12 data (7 mirrors)
14	12 data (6 mirrors) + 2 hot spares	14 data (7 mirrors)
16	14 data (7 mirrors) + 2 hot- spares	16 data (8 mirrors)
24 ^(c)	22 data (11 mirrors) + 2 hot spares	24 data (12 mirrors)
48	46 data (23 mirrors) + 2 hot spares	Not supported

- a. Not supported; an even number of drives is required by RAID 10
- b. 48-drive arrays cannot implement a no hot-spare policy
- c. PS4100/PS6100 models only

Table 14 Total reported usable storage when using hot spares: All models prior to PS4100/PS6100

Disk qty.	50 ^(a)	100 ^(a)	74	146	250	300	400	450	500	600	750	1000	2000	3000
7 ^(b)	-	-	-	-	666	-	-	-	-	-	-	-	-	-
8 ^(c)	-	-	-	-	666	-	-	-	-	-	-	-	-	-
14 ^(d)	-	-	395	-	1333	-	2135	-	2671	-	4013	-	-	-
16	309	621	-	913	1556	1867	2492	2805	3117	3744	4683	6247	12749	19230
48	-	-	-	-	-	-	-	-	10647	12779	-	21306	40840	63191

- a. Solid-state disk
- b. Legacy PS50E
- c. PS4000E and PS6000E only
- d. Legacy PS70E, PS100E, PS100X, PS200E, and PS300E
- e. PS6100S (SSD) only

Table 15 Total reported usable storage when using hot spares: PS41x0/PS61x0

Disk qty.	146	200 ^(e)	300	400 ^(e)	500	600	900	1000	1800	2000	2400	3000
6	-	-	-	-	887	1064	-	1781	-	3635	-	5498
12	650	920	1331	1843	2222	2672	4014	4454	-	9103	-	13762
14	781	-	1599	-	2670	3207	4816	5353	9632	-	12843	-
24	1433	2027	2938	4065	4904	5888	8841	9820	-	19998	-	30221

- a. Solid-state disk
- b. Legacy PS50E
- c. PS4000E and PS6000E only
- d. Legacy PS70E, PS100E, PS100X, PS200E, and PS300E
- e. PS6100S (SSD) only

Table 16 Total reported usable storage when using hot spares: PS4210

Drive qty.	146	300	400	600	800	900	1200	2400	2000	3000	4000	6000
12	-	-	-	-	-	-	-		9230 ^(a)	13845 ^(a)	18460 ^(a)	27690 ^(a)
24	-	3045 ^(b)	-	6091	-	9137 ^(c)	12183 ^(c)	24366 ^(c)	-	-	-	-

- a. 1 4210E only
- b. 2 4210XV only
- c. 3 4210X only

Table 17 Total reported usable storage when using hot spares: PS6210

Drive qty.	600	800	900	1200	1600	1800	2400	3000	4000	6000	8000
24	6091	8122 ^(e)	9137 ^(c)	12183 ^(c)	16224 ^(e)	18274 ^(c)	24366 ^(c)	30459 ^(b)	40612 ^(b)	60918 ^(b)	81224
	5890 ^(a)		8841 ^(a)			17628 ^(a)					

- a. SED
- b. 6210E only
- c. 6210X only
- d. 6210XV only
- e. 6210S only

Table 18 Total reported usable storage when using hot spares: PS6610

Drive qty.	600	900	1200	2400	2000	4000	6000	8000
42	-	16691 ^(b)	22220 ^(b)	44440 ^(b)	36966 ^(a)	74035 ^(a)	111001 ^(a)	151207 ^(a)
84	22732 ^(b)	34099 ^(b)	45465 ^(b)	90930 ^(b)	-	151654 ^(a)	227532 ^(a)	309981 ^(a)

- a. 6610E only
- b. 6610X only

7.3 RAID 50 drive layouts and total reported usable storage

Table 19 shows the drive layouts that are enforced when using a RAID 50 policy based on the number of drives in each array/hot spare configuration and the total usable storage available for each model.

RAID 50 (RAID 5 sets in a striped set) is created by first creating two or more RAID 5 sets and then striping data over those RAID 5 sets. RAID 50 implementations can tolerate a single drive failure per RAID 5 set.

Table 19 RAID 50 drive layouts and total reported usable storage available with hot spares (in GB)

Disk drives	Hot spare	No hot spare
6	5 data/parity + 1 hot spare	N/A ^(a)
7	6 data/parity + 1 hot spare	N/A ^(a)
8	6 data/parity + 2 hot spares	8 data/parity
12 ^(d)	10 data/parity + 2 hot spares	12 data/parity
14	12 data/parity + 2 hot spares	14 data/parity
16	14 data/parity + 2 hot spares	16 data/parity
24 ^(d)	22 data/parity + 2 hot spares	24 data/parity
48 ^(b)	46 data/parity + 2 hot spares	N/A ^(c)

- a. RAID 50 requires an even number of disk drives. A 7-drive configuration without hot-spare would result in odd number of disk drives.
- b. 48 drive arrays implement stripes across multiple RAID 5 sets within a single chassis.
- c. 48 drive arrays cannot implement a no hot-spare policy.
- d. PS4100/PS6100 models only.

Table 20 Total reported usable storage when using hot spares: All models prior to PS4100/PS6100

Drive qty.	50 ^(a)	100 ^(a)	74	146	250	300	400	450	500	600	750	1000	2000	3000
7 ^(b)	-	-	-	-	888	-	-	-	-	-	-	-	-	-
8(c)	-	-	-	-	888	-	-	-	-	-	-	-	-	-
14 ^(d)	-	-	660	-	2224	-	3566	-	4460	-	6695	-	-	-
16	532	1066	-	1566	2671	3207	4280	4817	5353	6427	8037	10719	21819	32972
48	-	-	-	-	-	-	-	-	18062	21677	-	36136	69260	107151

- a. Solid-state disk
- b. Legacy PS50E
- c. PS4000E and PS6000E only
- d. Legacy PS70E, PS100E, PS100X, PS200E, and PS300E
- e. PS6100S (SSD) only

Table 21 Total reported usable storage when using hot spares: PS41x0/PS61x0

Drive qty.	146	300	400 ^(e)	500	600	900	1000	1800	2000	2400	3000
6	-	-	-	1781	2129	-	3563	-	7280	-	11008
12	1044	2129	2949	3563	4280	6430	7137	-	14571	-	21975
14	1304	2670	-	4459	5353	8036	8930	16072	-	21429	-
24	2355	4815	6666	8038	9646	14474	16087	-	32727	-	49455

- a. Solid-state disk
- b. Legacy PS50E
- c. PS4000E and PS6000E only
- d. Legacy PS70E, PS100E, PS100X, PS200E, and PS300E
- e. PS6100S (SSD) only

Table 22 Total reported usable storage when using hot spares: PS4210

Drive qty.	146	300	400	600	800	900	1200	2000	2400	3000	4000	6000
12	-	-	-	-	-	-	-	14768 ^(a)	-	22152 ^(a)	29536 ^(a)	44304 ^(a)
24	-	4984 ^(b)	-	9968	-	14952 ^(c)	19936 ^(c)	-	39872 ^(c)	-	-	-

- a. 4210E only
- b. 4210XV only
- c. 4210X only

Note: RAID 50 is not a best practice for drives greater than or equal to 1TB and should not be considered for business-critical data. The Group Manager UI prevents RAID 50 over 3TB in size starting with v7 and up.

Table 23 Total reported usable storage when using hot spares: PS6210

Drive qty.	600	800	900	1200	1600	1800	3000	4000	6000	8000
24	9968	13291 ^(e)	14952 ^(c)	19936 ^(c)	26582 ^(e)	29904 ^(c)	49842 ^(b)	66456 ^(b)	99684 ^(b)	132912 ^(b)
	9646 ^(a)		14475 ^(a)							

- a. SED
- b. 6210E only
- c. 6210X only
- d. 6210XV only
- e. 6210S only

Note: RAID 50 is not a best practice for drives greater than or equal to 1TB and should not be considered for business-critical data. The Group Manager UI prevents RAID 50 over 3TB in size starting with v7 and up.

Table 24 Total reported usable storage when using hot spares: PS6610

Drive qty.	600	800	900	1200	2000	3000	4000	6000	8000
42	-	-	28243 ^(b)	37658 ^(b)	62764 ^(a)	-	128540 ^(a)	192811 ^(a)	257081 ^(a)
84	38766 ^(b)	-	58149 ^(b)	77532 ^(b)	-	-	264642 ^(a)	396963 ^(a)	529284 ^(a)

- a. 6610E only
- b. 6610X only

Note: RAID 50 is not available through the Group Manager UI on PS6610 arrays.

7.4 RAID 5 drive layouts and total reported usable storage

RAID 5 (striped disks with distributed parity) will combine N disks in an arrangement where each stripe consists of N–1 disks that contain data blocks plus 1 disk that contains a parity block. For each stripe, the parity block will be placed on a different disk ensuring that the parity blocks are not located on a single disk in the RAID set. RAID 5 implementations can tolerate a single drive failure without data loss.

Table 25 shows the drive layouts that are enforced when using a RAID 5 policy based on the number of drives in each array/hot spare configuration, and the total usable storage available for each model.

Note: RAID 5 is not supported for PS6210. To configure RAID 50 with drives larger or equal to 3TB, or RAID 5 with drives of any size, you must use the Group Manager CLI.

Table 25 RAID 5 drive layouts and reported usable storage available with hot spares (in GB)

Disk drives	Hot spare	No hot spare
6	5 data/parity + 1 hot spare	RAID policy for RAID 5 without a hot spare is not
7	6 data/parity + 1 hot spare	supported
8	7 data/parity + 1 hot spare	
12 ^(a)	11 data/parity + 1 hot spare	
14	13 data/parity + 1 hot spare	
16	15 data/parity + 1 hot spare	
24 ^{(a)(b)}	23 data/parity + 1 hot spare	
48 ^(b)	46 data/parity + 2 hot spares	

- a. PS4100/PS6100 models only
- b. 24 and 48 drive arrays implement multiple RAID 5 sets within a chassis

Table 26 Total reported usable storage when using hot spares: All models prior to PS4100/PS6100

Drive qty.	50 ^(a)	100 ^(a)	74	146	250	300	400	450	500	600	750	1000	2000	3000
7 ^(b)	-	-	-	-	1111	-	-	-	-	-	-	-	-	-
8 ^(c)	-	-	-	-	1333	-	-	-	-	-	-	-	-	-
14 ^(d)	-	-	792	-	2671	-	4281	-	5354	-	-	-	-	-
16	621	1244	-	1827	3118	3744	4995	5622	6247	7500	9378	12508	25456	38461
48	-	-	-	-	-	-	-	-	19452	23345	-	38916	74580	115394

- a. Solid-state disk
- b. Legacy PS50E
- c. PS4000E and PS6000E only
- d. Legacy PS70E, PS100E, PS100X, PS200E, and PS300E
- e. PS6100S (SSD) only

Table 27 Total reported usable storage when using hot spares: PS41x0/PS61x0

Drive qty.	146	200 ^(e)	300	400 ^(e)	500	600	900	1000	2000	3000
6	-	-	-	-	1781	2129	-	3563	7280	11008
12	1300	1843	2672	3696	4454	5355	8038	8929	18186	27473
14	1565	-	3206	-	5353	6426	9645	10719	-	-
24	2744	3880	5620	7772	9379	11253	16855	18728	38184	57698

- a. Solid-state disk
- b. Legacy PS50E
- c. PS4000E and PS6000E only
- d. Legacy PS70E, PS100E, PS100X, PS200E, and PS300E
- e. PS6100S (SSD) only

7.5 Array RAID configurations and associated RAID sets

The tables show a logical drive layout when an array is initialized for the first time. The actual physical layout of drives can change and evolve due to maintenance and administrative actions. Spare drives can move as they are utilized to replace failed drives and newly added drives become the spares. It is not possible to determine which physical drives are associated with each RAID set. This information is dynamic and maintained by the PS Series firmware.

Table 28 PS Series array RAID set relationship for each RAID type in a 24-drive configuration

RAID policy	Spare disks	RAID set relationship	Best practice
RAID 6	1 spare disk	(10+2) (9+2)	Yes
RAID 10	2 spare disks	(6+6) (5+5)	Yes
RAID 50	2 spare disks	(5+1, 5+1) (4+1, 4+1)	For selected configurations
RAID 5	1 spare disk	(12+1) (9+1)	Not for business-critical data

Table 29 PS Series array RAID set relationship for each RAID type in a 48-drive configuration

RAID policy	Spare disks	RAID set relationship	Best practice
RAID 6	1 spare disk	(12+2, 12+2, 12+2) (3+2)	Yes
RAID 10	2 spare disks	(7+7, 7+7, 7+7) (2+2)	Yes
RAID 50	2 spare disks	(6+1, 6+1, 6+1, 6+1, 6+1, 6+1) (3+1)	For selected configurations
RAID 5	2 spare disks	(12+1, 12+1, 12+1) (6+1)	Not for business-critical data

Table 30 PS Series array RAID set relationship for each RAID type in a 16-drive configuration

RAID policy	Spare disks	RAID set relationship	Best practice
RAID 6	1 spare disk	(13+2)	Yes
RAID 10	2 spare disks	(7+7)	Yes
RAID 50	2 spare disks	(6+1, 6+1)	For selected configurations
RAID 5	1 spare disk	(14+1)	Not for business-critical data

Table 31 PS Series array RAID set relationship for each RAID type in a 14-drive configuration

RAID policy	Spare disk	RAID set relationship	Best practice
RAID 6	1 spare disk	(11+2)	Yes
		(6+2 HDD, 3+2 SSD)*	
RAID 10	2 spare disks	(6+6)	Yes
RAID 50	2 spare disks	(5+1, 5+1)	For selected configuration
RAID 5	1 spare disk	(12+1)	Not for business-critical data

^{*}Applies to the PS-M4110XS hybrid array configuration.

Table 32 PS Series array RAID set relationship for each RAID type in an 84-drive configuration

RAID policy	Spare disks	RAID set relationship	Best practice
RAID 6	2 spare disks	(12+2) (12+2) (12+2) (12+2) (12+2) (10+2)	Yes
RAID 10	2 spare disks	(12+2) (12+2) (12+2) (12+2) (12+2) (10+2)	Yes
RAID 50	2 spare disks	(12+2) (12+2) (12+2) (12+2) (12+2) (10+2)	Not for business-critical data
RAID 5	2 spare disks	(12+2) (12+2) (12+2) (12+2) (12+2) (10+2)	Not for business-critical data

Table 33 PS Series array RAID set relationship for each RAID type in a 42-drive configuration

RAID policy	Spare disks	RAID set relationship	Best practice
RAID 6	2 spare disks	(12+2) (12+2) (10+2)	Yes
RAID 10	2 spare disks	(12+2) (12+2) (10+2)	Yes
RAID 50	2 spare disks	(12+2) (12+2) (10+2)	Not for business-critical data
RAID 5	2 spare disks	(12+2) (12+2) (10+2)	Not for business-critical data

PS Series SAN design 8

A PS Series iSCSI SAN can be operated in any network that supports the industry standards and IP subnet design guidelines described in this section. Because of this flexibility, there are many network design and configuration choices that can affect SAN performance. The following sections provide details related to network design and configuration to support the use of a PS Series SAN.

Note: With the addition of FS Series NAS functionality, the PS Series product family now provides an iSCSI based unified file and block storage platform. A PS Series SAN can now be used to provide block level access (direct iSCSI access to PS Series arrays) or file system level access (via the FS Series appliance) using NFS or CIFS protocols and the Dell FluidFS scale-out file system.

Unless otherwise stated, SAN recommendations in this document are applicable to both file and block environments.

8.1 General requirements

8.1.1 Implementation of standards

PS Series SANs are based on industry standards. The following standards are required to support all host to target communications and member to member communications in a PS Series SAN:

- **IETF Standards**
 - IETF RFC1122 "Requirements for Internet Hosts Communications Layers"
 - IETF RFC1123 "Requirements for Internet Hosts Application and Support"
 - IETF RFC3270 "Internet Small Computer Systems Interface (iSCSI)"
- **IEEE Standards**
 - 802.1
 - 802.3

iSNS support

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An Internet Storage Name Service¹ (iSNS) server can support discovery, management and configuration of group members by dynamically updating information about the iSCSI target names for group volumes. Once the IP address of an iSNS server is entered in an iSCSI initiator's configuration utility, the setting is persistent across initiator sessions. A PS Series group can be configured to register with up to three iSNS servers.

Note: Starting with Firmware V4.1.4, volume and snapshot identifiers are no longer automatically published to iSNS servers. This applies to new volumes and snapshots as well as volumes and snapshots that existed before the group was upgraded to V4.1.4.

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¹ The Internet Storage Name Service (iSNS) specification: http://tools.ietf.org/html/rfc4171

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8.1.2 General requirements and recommendations

For PS Series arrays, the following general SAN design recommendations apply:

- To ensure a secure network environment, Dell EMC strongly recommends the following:
 - The network environment in which the group resides should be secure from network attacks such as packet sniffing or connection hijacking. This includes network hardware such as switches and routers. Firewalls and network isolation should be employed to protect resources. Network security can be achieved in a number of ways depending on the level of assurance needed and the types of network attacks to be expected. Security mechanisms include physical security, application level encryption (SSL/TLS and SSH), and/or IP level encryption (IPsec).
 - Network protocols and services used by the group should be secure and not subject to attacks.
 This includes RADIUS authentication, Network Time Protocol (NTP), syslog, and outbound SMTP traffic.
- For all members (arrays) in a given SAN Group all ports should be connected to the same subnet. This allows the arrays to communicate with each other as a group of peer members. The arrays must be in the same subnet as the PS Series Group IP address.

Note: Hosts can be in a different subnet as long as those hosts have layer 3 routing available to the subnet containing the arrays and the group's well-known IP address.

• It is strongly recommended that a physically separated network be used for iSCSI traffic and that this network not be shared with other traffic types.

Note: If there is a requirement to share the same physical networking infrastructure with other non-iSCSI traffic then Data Center Bridging (DCB) is the recommended method for sharing networking resources.

- Rapid Spanning Tree Protocol must be enabled if the SAN infrastructure has more than two switches
 in a non-stacked configuration, and PortFast must be enabled on all edge device ports (hosts, FS
 Series appliances and arrays).
- Port density requirements to support fully redundant configurations and maximum SAN throughput are as follows:
 - **PS4x00** family: 2x 1GbE ports per controller = 4x 1GbE ports total
 - **PS4110 family**: 1x 10GbE port per controller = 2x 10GbE ports total
 - PS4210 family: 2x 10GBASE-T and 2x 10GbE SFP+ ports per controller = 8x 10G ports total
 - PS5x00 family: 3x 1GbE ports per controller = 6x 1GbE ports total
 - **PS6x00** family: 4x 1GbE ports per controller = 8x 1GbE ports total
 - PS6510 family: 2x 10GbE ports per controller = 4x 10GbE ports total
 - **PS6110 family**: 1x 10GbE port per controller = 2x 10GbE ports total
 - **PS6210 family:** 2x 10GBASE-T and 2x 10GbE SFP+ ports per controller = 8x 10G ports total
 - **PS6610 family:** 2x 10GBASE-T and 2x 10GbE SFP+ ports per controller = 8x 10G ports total
 - FS7500 NAS: 12x1GbE ports per controller node (four client LAN and eight iSCSI SAN) + 1 100/1000Mb port per controller node for IPMI interconnection
 - FS7600 NAS Appliance: 16x1GbE Ethernet ports per appliance = 8 x 1Gb Ethernet ports per NAS controller for client connectivity and 8 x 1GbE Ethernet ports per NAS controller for SAN connectivity

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- FS7610 NAS Appliance: 8x10GbE Ethernet ports per appliance = 4 x 10GbE SFP+ Ethernet ports per NAS controller for client connectivity and 4 x 10GbE SFP+ Ethernet ports per NAS controller for SAN connectivity
- At least two iSCSI SAN ports per host (block level iSCSI access) are required for fully redundant SAN connectivity. Host ports can be 1GbE or 10GbE and the host speed should match the array port speed.
- Quality of Service (QoS) based on what is traditionally designated as IEEE 802.1p is not currently supported for use with PS Series SANs. QoS and Class of Service designations must be disabled.
- All switches within the SAN must be interconnected such that there is always a path from any Ethernet port on one array to all other Ethernet ports on all other arrays in the group.
- All switches and host network controllers within the infrastructure must have (at a minimum, receive) flow control enabled for optimal performance.
- Any PS Series SAN group that is required to send or receive replication traffic to/from another SAN group must have an uninterrupted communications path (visibility) between each group.
- To prevent a switch failure from also disabling all paths between a host and its connected volumes, all ports from each controller need to be connected to at least two different physical switches. This also applies to LAN side connections in FS Series NAS appliance configurations. (Refer to the connection diagrams in Section 12.1)
- For PS4100/PS6100 family arrays, split the vertical port pair connections between two switches to ensure 100% bandwidth capability is maintained in the event of a vertical port failover event.
- Do not block IPv6 discovery traffic on the SAN internal switches when utilizing FS Appliances FS76x0 uses IPv6 discovery for internal communication and setup only; not for user data. Client communication occurs over IPv4
- For SANs connecting to an FS Series appliance, all switches in the SAN must have jumbo frames enabled
- Take advantage of your switch's VLAN capabilities. You may optionally create a VLAN dedicated to iSCSI traffic (even on dedicated switches). If necessary, create a second VLAN for management traffic. The actual VLAN configuration of your iSCSI SAN will be dictated by your SAN network design requirements and the features of the iSCSI SAN switches being used.
- Jumbo frames should be enabled for best performance. If you choose to use jumbo frames then all nodes in the SAN fabric must have Jumbo frames enabled.
- For best performance and reliability, we recommend that all interconnection paths between nonstacking switches (LAGs) use a dynamic link aggregation protocol such as LACP

8.1.3 Quality of service (QoS)

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QoS is described as either of the following:

- The ability to provide different priority levels to different applications, users, or data flows, or to guarantee a certain level of performance to a data flow.
- A network function implemented in some routers and switches that provides high priority for certain types of latency-sensitive traffic (for example, VoIP) and lower priority for other types of traffic (for example, web or http).

PS Series arrays are designed to provide I/O as fast as your network can support it. Therefore, using QoS with iSCSI traffic does not produce expected or desirable results on the SAN. Also, QoS rules can affect how well—or even whether—replication succeeds between PS Series groups. If you plan to use QoS, Dell EMC recommends that you use it only on Wide Area Network (WAN) connections, where bandwidth is shared with other applications and the PS Series array uses it for time-insensitive replication traffic.

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Dell EMC recommends against using QoS on the SAN switches for initiator-target communications paths.

8.2 Ethernet switches and infrastructure

Any switch used in a PS Series SAN should meet the requirements listed in this section.

Note: For a complete list of tested and validated components see the *Dell EMC Storage Compatibility Matrix*. Also review the included support policy statement.

A PS Series SAN consists of one or more hosts connected to one or more PS Series arrays through a switched Ethernet network.

Note: The minimum network configuration for a PS Series array consists of a connection between Eth0 on each control module and a network switch. To increase performance and availability, configure multiple network interfaces on an array and connect them to multiple switches. PS Series arrays do not support Direct Attached Storage (DAS) configurations.

To support a high-performance PS Series SAN, switches must meet the following general requirements:

- **Low latency:** Switches with relatively high latency may cause SAN throughput performance to degrade, and under high load conditions they could increase the risk of dropped connections.
- Non-blocking backplane design: SAN Switches should be able to provide the same amount of backplane bandwidth to support full duplex communication on ALL ports simultaneously.
- Link Layer Discovery Protocol (LLDP): If any non-array devices on the SAN network have LLDP
 enabled, then the switch must have LLDP support enabled. If the SAN switch does not provide LLDP
 support, then disable LLDP on all non-array devices connected to the switch. For specific instructions
 on disabling LLDP on your devices, refer to the user manual of the device.
- Adequate buffer space per switch port: In addition to supporting data transfers between the hosts and the SAN, PS Series arrays also use the SAN to support inter-array communication and data load balancing. For this reason, the more buffer space per port that a switch can provide the better. Due to the multitude of buffer implementations used by switch vendors, Dell cannot provide definitive guidelines as to how much is enough. Port buffers should be designed such that data is not lost when traffic reaches extreme levels. Due to the clustered storage traffic patterns used by the PS Series SAN architecture, switches that support "cut-through" mode are not suitable for use in a PS Series SAN and may actually result in lower overall SAN performance.
- Support for IEEE 802.3x flow control (passive and/or active) on ALL ports: Switches and
 network interface controllers used in a PS Series SAN must be able to passively respond to any
 "pause" frames received. All ports should have at minimum "receive", "honor", "respond" or "RX"
 flow control enabled. Host ports should be configured as "RX" and "TX" or "symmetric" flow control
 enabled. Array ports do not require TX flow control.
- Support for Jumbo frames: This is not a requirement, however most iSCSI SAN implementations should benefit from using Jumbo frames and therefore it is recommended to enable Jumbo frames.
 The actual impact on SAN throughput when using jumbo frames will depend on your workload's I/O characteristics.
- Support for Rapid Spanning Tree protocol (IEEE 802.1w), with edgeport or Cisco "PortFast" functionality if the SAN infrastructure will consist of more than two switches: For SAN infrastructures consisting of more than 2 non-stacking switches, R-STP must be enabled on all ports used for inter-switch trunks. All non-inter-switch trunk ports should be marked as "edge" ports or set to "PortFast".

- Support for unicast storm control: iSCSI in general, and PS Series SANs in particular can send packets in a very "bursty" profile that many switches could misdiagnose as a virally induced packet storm. Since the SAN should be isolated from general Ethernet traffic, the possibility of actual viral packet storms occurring is non-existent. In a PS Series SAN, the switches must always pass Ethernet packets regardless of traffic patterns. Therefore, the switch should have the ability to disable unicast storm control response.
- Support for stacking: A switch interconnection is required to link all switches in the SAN
 infrastructure together. Some Ethernet switches feature dedicated stacking interfaces. A good rule of
 thumb for a dedicated stacking link bandwidth would be a minimum 20 Gbps full-duplex. Depending
 on your SAN availability requirements, stacking may or may not be a suitable solution for switch
 interconnect.
- Support for VLAN functionality

Note: It is recommended to use a physically separated network dedicated to iSCSI traffic that is not shared with other traffic. If sharing the same physical networking infrastructure is required, then use Data Center Bridging (DCB) for PS Series SAN.

• Support for creating Link Aggregation Groups (LAG): For non-stacking switches, or where stacking of switches does not support your availability requirements, the ability to bind multiple physical ports into a single logical link for use as an interconnection is required. The switch should support designating one or more ports for interconnection (via LAGs). The switch should support creation of LAGs consisting of at least eight 1Gbps ports or at least two 10Gbps ports. Variants of LAG (such as VLT, or vPC) may also be supported if the feature supports the spanning of data streams across the interconnect variant. Please consult your network specialist to determine the capabilities of these interconnect variants. At this time, Dell Networking's VLT and Cisco's vPC support this requirement. Some versions of MLAG may not.

Note: For 1GbE SANs, using non-stacking switches to connect three or more PS Series arrays into a single group may negatively impact SAN I/O throughput performance.

8.2.1 Connecting SAN switches in a Layer 2 network

PS Series storage requires that the SAN be configured as a single Layer 2 network. Layer 2 refers to the data link layer in the OSI model (http://en.wikipedia.org/wiki/OSI_model). When more than one SAN switch is required, each switch connected to the array group members will be in the same subnet. These switches must be interconnected to provide a single switched Ethernet fabric. Figure 4 shows the two common methods for interconnecting switches, using either stacking switches or non-stacking switches.

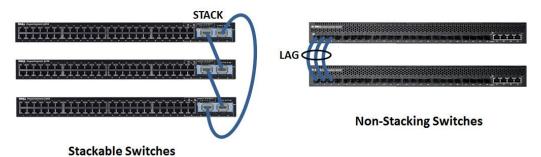


Figure 4 Switch Interconnects

8.2.1.1 Stacking switches

Stacking switches provides a simple method for creating a switch interconnection within a Layer 2 network infrastructure. Stacking is typically accomplished using a vendor proprietary, high-bandwidth, low-latency interconnect that allows two or more switches to be connected in such a way that each switch becomes part of a larger, virtual switch. Most stackable switches will provide a set of dedicated stacking ports or Installation of an optional stacking module may be required. Some switches also provide virtual stacking modes using standard Ethernet ports.

8.2.1.2 Non-stacking switches

Non-stacking switches do not have a dedicated switch interconnect. In this case to create an interconnect between the switches you must utilize one or more ports on each switch to create a LAG. This type of switch interconnection should use link aggregation functions (if provided by the switch) to aggregate multiple Ethernet ports into a single, logical high bandwidth interconnect. There are several options depending on the vendor of the switch. Table 34 describes the most common options.

Note: For best performance and reliability, use a dynamic link aggregation protocol such as LACP for all interconnection paths between non-stacking switches (LAGs).

Table 34 Link aggregation types

Link aggregation type	Notes
Static	Static link aggregation defines a set of links that provide a point to point connection between two switches. These links may or may not provide failover redundancy or traffic load management.
LACP	Link Aggregation Control Protocol is based on IEEE 802.3ad or IEEE 802.1AX. LACP is a dynamic LAG technology that automatically adjusts to the appearance or disappearance of links within the defined LACP group.
PAgP	Port Aggregation Protocol (PAgP) is a Cisco Systems® proprietary networking protocol, which is used for the automated, logical aggregation of Ethernet switch ports, known as an etherchannel. This means it can only be used between Cisco switches and/or switches from licensed vendors.
Vendor Proprietary	Several switch vendors may provide additional link aggregation options that are completely proprietary or may be extensions to one of the two previously mentioned solutions. In most cases, this type of link aggregation solution is designed to reduce or eliminate the need – and the overhead – of the Spanning Tree Protocol that must be used in the two previous options. If available, these proprietary options should be considered. They may be very useful in allowing the network administrator to create a more efficient multi-switch layer 2 network infrastructure for a SAN. Be aware that these proprietary solutions must support the transmission of IEEE 802.1x flow control and jumbo frames (if used) to properly support a PS Series SAN.
	Examples of this type of interconnect include Dell Networking or Force10 Virtual Link Trunking (VLT) and Cisco Virtual Port Channel (VPC).

8.2.1.3 Using a LAG to Connect Stacked Switches

In some situations it may become necessary to expand the PS Series SAN by using more than one single switch stack. For example, you can link multiple stacks by creating a multi-link LACP based LAG between the switch stacks. A simplified stack plus LAG switch configuration is illustrated in Figure 5. You should consider the following recommendations when designing this type of SAN:

- If possible, use 10Gb or higher connections for all links between each stack.
- Distribute the links across multiple switches within each stack.
- Use LACP or another type of dynamic link aggregation protocol.
- Perform tests to determine the best hashing algorithm to use within the LAG (port channel).

Note: A multi-stack SAN infrastructure as described in this section may provide added reliability to the SAN environment. But, it may also introduce additional latency and the potential for lower throughput. The SAN designer will have to carefully consider the performance and reliability implications.



Figure 5 Using a LAG to interconnect switch stacks

8.2.2 Sizing inter-switch connections

Use the guidelines in Table 35 as a starting point for estimating inter-switch connection sizes.

Table 35 Switch Interconnect Design Guidelines

Connection speeds	Interconnection guidelines
1GbE switches attached to	1-5 arrays: 1Gb of inter-switch bandwidth per active array controller port (up to the aggregated maximum
1GbE array controllers	bandwidth of the inter switch links.
	6+ arrays : Use 1-5 array rule, then add 1Gb of additional bandwidth for each array added
10GbE switches attached to	PS6010/PS6510 or PS4110/6110 (Random Small Block Workloads):
10GbE array controllers	,
·	1-5 arrays : 20 – 30Gb of inter-switch bandwidth between each switch
	6+ arrays: At least 6Gb of inter-switch bandwidth per array between each switch
	PS4110/PS6110 (Large Block Sequential Workloads):

Connection speeds	Interconnection guidelines	
	1-5 arrays: 10Gb of bandwidth per active array controller port (up to the aggregated maximum bandwidth of the IST).	
	6+ arrays: Use 1-5 array rule, then add 10Gb of additional bandwidth for each array added	

8.2.3 Comparing inter-switch connection types

Table 36 provides details and recommendations for selecting interconnect options.

Table 36 Stacking versus LAG

	DIE 36 Stackling Versus LAG			
Interconnect type	Primary purpose	Analysis		
Stacking	Create a larger, logical switch within an isolated physical location.	Advantages: Easier to manage multiple switches as single switch Possibly higher bandwidth than using link aggregation and Ethernet Not limited by Ethernet standards Firmware upgrades will reset the entire stack Concerns: Proprietary, cannot be used to interconnect switches from different vendors Increases cost of switch Stack bandwidth overload risk Recommendation: Simplest way to scale a storage network in a single location on a single subnet May provide lower latency and higher bandwidth than a LAG Understand the stacking technology and limit stack size in accordance with total throughput requirements and connection counts.		

Interconnect type	Primary purpose	Analysis
LAG	Create a data path between switches in one location or subnet with those in another location or subnet	 Advantages: Leverages Ethernet standard extensions Can be used to interconnect switches from different vendors Can use Link Aggregation Protocols (LACP/EtherChannel) to pool multiple 1GbE or 10GbE links into a single logical link providing bandwidth and redundancy Can upgrade switch firmware on each switch independently Concerns: Some solutions limited to eight port link aggregation group Spanning Tree Protocol must be used if more than two switches are used causing some links to be "blocked" and reducing bandwidth availability LAG bandwidth overload risk Recommendation: Use when stacking is not available Use when connecting to aggregation/core switching infrastructure Use when switches are from different vendors Use when highest availability is required

9 Building a high-availability SAN

Designing a redundant SAN requires the availability of redundant NICs or HBAs on each server. A redundant NIC configuration on the server requires at least two NICs.

The information provided here does not address all of the possible variations in a SAN.

All information is presented using a set of basic reference designs that make the following assumptions:

- The SAN network is physically isolated from all other network traffic
- The examples are based on best practice design principles.
- Unless otherwise stated, all reference designs will provide end-to-end host to volume redundant paths
- A minimal number of switches will be illustrated to allow for the design concept to be understood.
 Actual implementations will vary depending on your network infrastructure requirements.
- If sharing physical switches with other non-SAN traffic, we assume all switches are VLAN capable.

Note: The PS4110 and PS6110 PS Series Arrays do not support dual speed connectivity. They are not designed to support the auto-negotiate feature. The connectivity must be 10Gb ONLY - This is the only supported configuration.

9.1 Multi-path I/O

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There are generally two types of multi-path access methods for communicating from a host to an external device. For general networking communications, the preferred method of redundant connections is the teaming of multiple NICs into a single, virtual network connection entity. For storage, the preferred method of redundant connection is the use of Multi-Path I/O (MPIO). Though some storage solution can and do support either method for iSCSI connectivity, PS Series storage requires the use of MPIO to enable multiple NIC/HBA connections to be utilized for access to a PS Series SAN.

The MPIO ToolKits, for available Operating Systems, can be found at the link below: (for example, the Host Integration ToolKit for Microsoft, the Host Integration Tools for Linux and the Multipathing Extension Module for vSphere VMware ESXi). https://eglsupport.dell.com/support/download.aspx

Click on the Host Integration Tools for your Operating System.

Solution guides for using PS MPIO on various operating systems (OSes) are available from the Dell TechCenter. These solutions guides include:

- Configuring and Deploying the Dell PS Series Multi-Path I/O Device Specific Module with Microsoft Windows Servers
- Configuring and Installing the PS Series Multipathing Extension Modules for VMware vSphere and PS Series SANs
- Using HIT/Linux and ASM/LE with Dell PS Series Storage

Note: PS Series storage does not support NIC teaming on interfaces connected to the SAN.

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9.1.1 PS Series MPIO requirements

The following host port requirements must be met to use MPIO with PS Series SANs:

- At least two Ethernet ports are required on each host. While not required, it is preferable that these
 ports be on different physical adapters to ensure a highly available SAN solution.
- The host operating system must have a supported MPIO driver or service available.
- The ports used for MPIO cannot be "teamed" to other ports.
- The ports must be the same speed
- The ports must be assigned IP addresses on the same subnet

9.1.2 PS Series MPIO general recommendations

Follow this general set of guidelines for configuring MPIO on a host:

- Configure volume access controls to use standard iSCSI IQN names
- For a more secure configuration you can use the IQN name plus the CHAP security ID.
- On each array enable at least two ports for host connectivity.
- Install the Dell EMC provided MPIO extension features, if available for the host operating system
- For Microsoft Windows, install the Device Specific Module (DSM) found in the Host Integration Toolkit for Windows.
- For VMware vSphere ESXi, install the Dell PS Series Multipathing Extension Module.
- For Linux (supported versions), use the MPIO functionality provided by the Host Integration Toolkit for Linux.
- For other operating systems, use the native MPIO functionality.

9.2 Redundant SAN configuration

In a redundant iSCSI SAN, each component of the SAN infrastructure has a redundant connection or path. The following figures show example connection paths necessary to create a Redundant SAN.

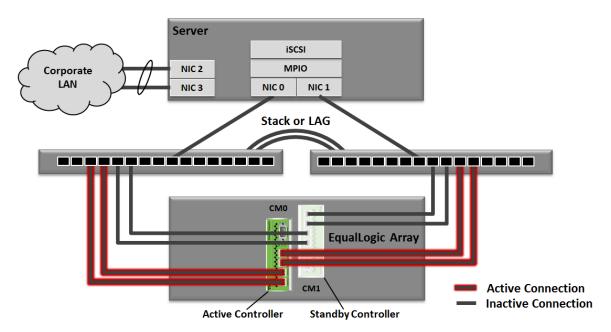


Figure 6 Redundant SAN Connection Paths: PS3000 to PS6500 Family Arrays

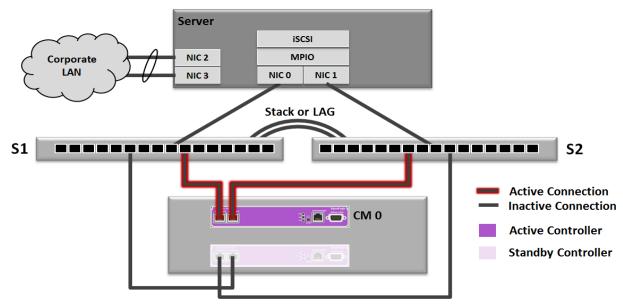


Figure 7 Redundant SAN Connection Paths: PS4100

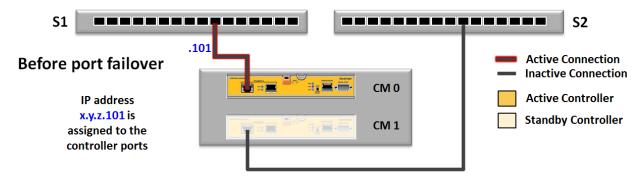


Figure 8 Redundant SAN Connection Paths: PS4110/PS6110

9.3 Controller redundancy in all models

Each array can be configured with either a single controller, or dual redundant controllers. The single controller configuration will provide the same level of I/O performance as a dual controller configuration. The dual controller configuration provides for redundancy. Dual controllers provide redundancy in the case of a controller failure. Host connections will fail over to the redundant controller.

The active controller is the one processing all disk and network I/O operations for the array. A second controller in dual controller configurations will always be in a passive operating mode. In this mode, the secondary controller exhibits the following characteristics:

- Each of its Ethernet ports are electrically inactive (active lights are off), unless a vertical port failover has occurred.
- The passive controller cache mirrors the cache of the active controller.

9.4 Basic controller failover behavior in all models

To support redundant controller fail over, each Ethernet port on the active controller that is connected to the SAN must have its corresponding port on the passive controller also connected to the same SAN network. In the event of a controller failure, the passive controller will immediately activate and continue to process all data requests to the array. The following changes occur during fail over:

- The backup controller automatically enables each of the corresponding Ethernet ports that were enabled on the failed primary controller
- The IP addresses that were assigned to each of the failed controller Ethernet ports are reassigned to the corresponding ports on the second controller.

9.4.1 Basic controller failover for models prior to PS4100/PS6100

A link failure on one or more of the Ethernet ports on the active controller does not constitute a controller failure. For this reason, it is important to ensure that ports from each controller are connected to at least two different switches. This will prevent a switch failure from also disabling all paths between a host and its connected volumes.

It is also critical that port connections for both controllers are configured so that the corresponding ports on each controller are connected to the SAN. If port 0 on the active controller is the only port connected to the SAN, then you must use port 0 on the passive controller for the other connection to the SAN. This is illustrated in the partially connected scenario shown in Figure 9.

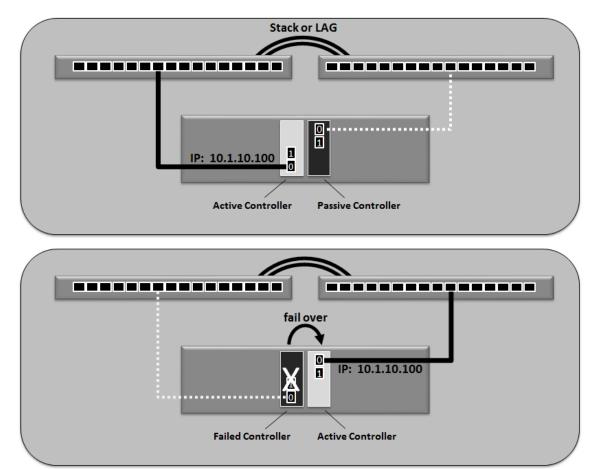


Figure 9 Partially connected controller failover

Note how IP addresses are reassigned to their corresponding ports during the failover processes shown in Figure 9 and Figure 10.

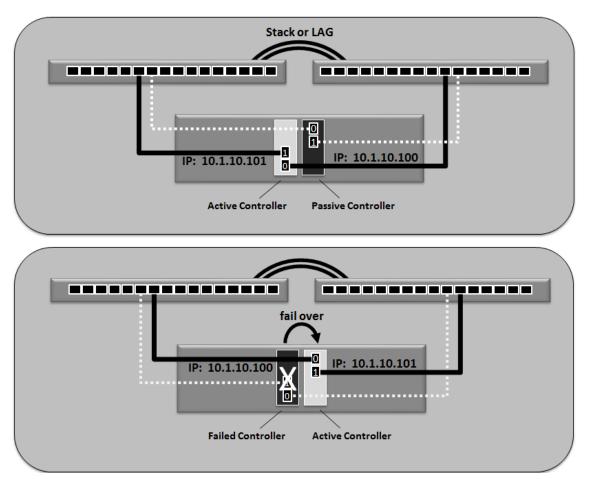


Figure 10 Fully connected controller failover

9.4.2 Controller failover behavior for: PS42x0/PS41x0/PS62x0/PS61x0/PS6610

In the event of a controller failure the following changes occur:

- The passive controller immediately activates and continues to process all data requests to the array.
- Vertical port failover ensures that IP addresses assigned to each of the failed controller Ethernet ports
 apply to the corresponding ports on the second controller.

As stated in Section 9.4.1, controller models prior to the PS4100/PS6100 required having cabled connections present on corresponding controller ports for controller failover to work correctly. The vertical port failover feature in PS42x0/PS41x0/PS62x0/PS61x0/PS6610 controllers eliminates this requirement, enabling partially cabled configurations to support controller failover too.

It is important to ensure that ports from each controller are connected to at least two different switches. This will prevent a switch failure from also disabling all paths between a host and its connected volumes. We illustrate controller failover behavior for the PS4200/PS4100 family controller in Figure 11. Controller failover behavior for the PS6200/PS6100 (4 port) controller family is identical.

Note: To prevent a switch failure from also disabling all paths between a host and its connected volumes, you should make sure that ports from each controller are connected to at least two different switches. You should also split vertical port pair connections between two switches to ensure 100% bandwidth capability is maintained in the event of a vertical port failover event. Both of these guidelines are illustrated in Figure 11.

Controller Failover Scenario **S1 S2** .101 102 **Active Connection** Before port failover **Inactive Connection Active Controller** IP addresses in the range x.v.z.101-102 **Standby Controller** assigned to the controller ports **S1 S2** .102 .101 After failover CM₀

Figure 11 Controller failover process and optimal connection paths

9.4.3 Failover behavior in the PS-M4110

Each PS-M4110 array is configured with dual controllers, active and passive connections, and redundant fabrics to provide redundancy and prevent volume connections between hosts and SAN from being dropped in the event of a failure.

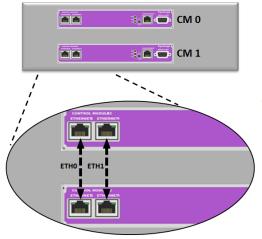
There are three failure scenarios that are protected by the array:

- In the event of a link failure, the active link of the active port goes down, and the standby link for the same port takes over.
- In the event of a switch failure, both active and passive ports automatically link to the redundant fabric switch.
- If the active controller fails, the passive controller takes over.

9.5 Vertical port failover behavior in PS4100/PS6100 controllers

In PS Series controllers prior to PS4100/6100 families, a link failure or a switch failure was not recognized as a failure mode by the controller. Thus, a failure of a link or an entire switch would reduce bandwidth available from the array. Referring to Figure 12 or Figure 13, assume that CM0 is the active controller. In vertical port failover, if CM0 senses a link drop on the local ETH0 port connection path, it will automatically begin using the ETH0 port on the backup controller (CM1) without incurring a controller module failover.

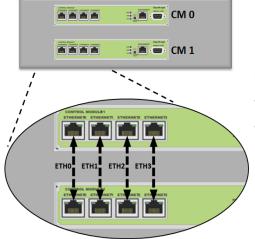
Vertical port failover is bi-directional. If CM1 is the active controller then vertical port failover will occur from CM1 ports to CM0 ports if necessary.



PS4100 Vertical Port Failover

• Both controller ports support vertical failover

Figure 12 PS4100 vertical port failover



PS6100 Vertical Port Failover

- · All controller ports support vertical failover
- · A maximum of two ports can failover at one time

Figure 13 PS6100 vertical port failover

With PS4100/PS6100 family controllers, vertical port failover can ensure continuous full bandwidth is available from the array even if you have a link or switch failure. This is accomplished by combining corresponding physical ports in each controller (vertical pairs) into a single logical port from the point of view of the active controller. In a fully redundant SAN configuration, you must configure the connections as shown in Section 9.2.

In a redundant switch SAN configuration, to optimize the system response in the event you have a vertical port failover you must split the vertical port pair connections between both SAN switches. The connection paths illustrated in Figure 14 and Figure 15 show how to alternate the port connection paths between the two controllers. Also note how IP addresses are assigned to vertical port pairs.

PS4100: Vertical Port Failover Scenario

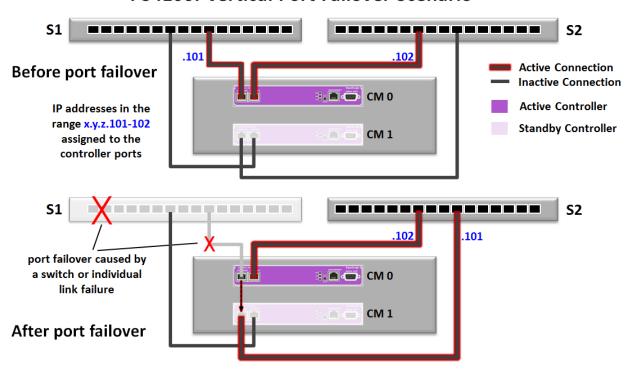


Figure 14 PS4100 vertical port failover and optimal connection paths

IMPORTANT: By alternating switch connection paths between ports in a vertical port pair, port failover allows the array to maintain 100% bandwidth capability in the event of a switch failure.

S1 **S2** .102 .103 Before port .101 .104 **Active Connection** failover **Inactive Connection** CM₀ **Active Controller** IP addresses in the range x.y.z.101-104 **Standby Controller** CM₁ assigned to the controller ports _____ **S2** .103 104 port failover caused by a switch or individual EM 0 link failure CM₁ After port failover .101

PS6100: Vertical Port Failover Scenario

Figure 15 PS6100 vertical port failover process and optimal connection paths

9.6 Vertical port failover behavior in PS4110/PS6110 controllers

In PS Series controllers prior to PS4110/PS6110 families, a link failure or a switch failure was not recognized as a failure mode by the controller. This caused a failure of a link or an entire switch to reduce bandwidth available from the array. Referring to Figure 12 or Figure 13, assume that CM0 is the active controller. In vertical port failover, if CM0 senses a link drop on the local ETH0 port connection path, it will automatically begin using the ETH0 port on the backup controller (CM1). Vertical port failover is bi-directional. If CM1 is the active controller then vertical port failover will occur from CM1 ports to CM0 ports if necessary.

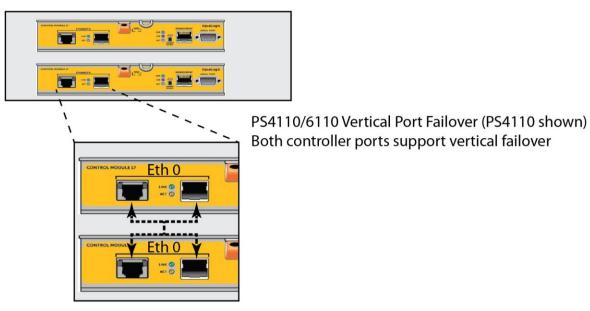


Figure 16 4110/6110 vertical port failover

With the PS4110/PS6110 family of controllers, vertical port failover can ensure continuous full bandwidth is available from the array even if you have a link or switch failure. This is accomplished by combining 10GbE "eth0" ports in each controller into a single logical port from the point of view of the active controller. In a fully redundant SAN configuration, you must configure the connection as shown in Figure 17.

In a redundant switch SAN configuration, to optimize the system response in the event you have a vertical port failover, you must connect either the SFP+ cable or the 10GbaseT cable from each controller to a different switch in the SAN network. While it is a supported configuration, it is not recommended to connect both the SFP+ and 10GbaseT ports on each controller at the same time. In this scenario, the SFP+ port will always be the preferred active port and this preference cannot be changed by the user. This preference is not guaranteed in future array firmware releases.

The connection paths illustrated in Figure 17 show how to connect the port connection paths between the two controllers. Also note how IP addresses are assigned to the vertical port pair.

PS4110/PS6110: Vertical Port Failover Scenario

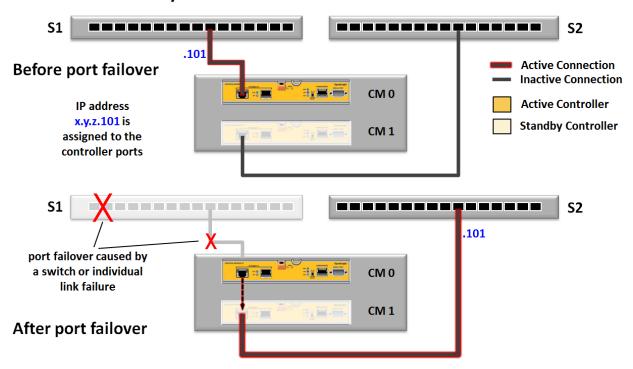
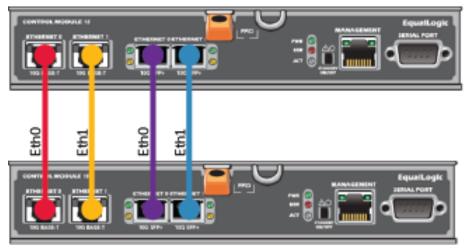


Figure 17 4110/6110 Vertical port failover scenario

9.7 Vertical port failover behavior in PS4210/PS6210/PS6610 controllers

In PS Series controllers prior to PS4110/6110/6210 families, a link failure or a switch failure was not recognized as a failure mode by the controller. This caused a failure of a link or an entire switch to reduce bandwidth available from the array. Referring to Figure 13, assume that CM0 is the active controller. In vertical port failover, if CM0 senses a link drop on the local ETH0 port connection path, it will automatically begin using the ETH0 port on the backup controller (CM1). Vertical port failover is bi-directional. If CM1 is the active controller then vertical port failover will occur from CM1 ports to CM0 ports if necessary.

Note: The vertical port failover behavior exhibited in PS4210/PS6210 controllers also applies to the PS6610.



PS6210 Vertical port failover

- All controller ports support vertical failover
- A maximum of two ports can failover at one time

Figure 18 PS6210 vertical port failover

For more information about vertical port failover, see the *PS4210 Hardware Owner's Manual* or *PS6210 Hardware Owner's Manual* or *PS6610 Hardware Owner's Manual* at https://eqlsupport.dell.com/support/resources.aspx?id=2495 (requires support login). With the PS4210/PS6210/PS6610 family of controllers, vertical port failover can ensure continuous full bandwidth is available from the array even if you have a link or switch failure. This is accomplished by combining each 10GbE "eth0" and "eth1" ports in each controller into a single logical port from the point of view of the active controller. In a fully redundant SAN configuration, you must configure the connection as shown in Figure 18.

In a redundant switch SAN configuration, to optimize the system response in the event you have a vertical port failover you must connect either the SFP+ cable or the 10GbaseT cable from each controller to a different switch in the SAN network. While it is a supported configuration, it is not recommended to connect both the SFP+ and 10GbaseT ports on each controller at the same time. In this scenario, the SFP+ port will always be the preferred active port and this preference cannot be changed by the user. This preference is not guaranteed in future array firmware releases.

The connection paths illustrated in Figure 18 show how to connect the port connection paths between the two controllers. Also note how IP addresses are assigned to the vertical port pair.

PS6210: Vertical Port Failover Scenario

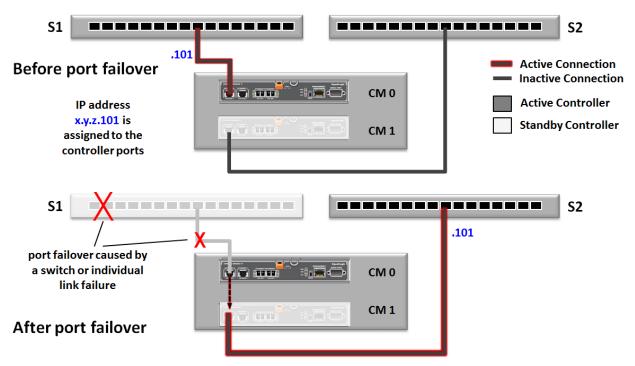


Figure 19 PS6210 Vertical port failover scenario

Mixed-speed environments: Integrating 1GbE and 10GbE SANs

With the introduction of 10GbE, some situations that require 1Gb arrays and 10Gb arrays coexisting in the same SAN infrastructure. PS Series arrays support operation of 1Gb and 10Gb arrays within the same group. This section summarizes mixed speed SAN design guidelines that are presented in the following publications:

- PS Series Architecture MPIO with Devices Having Unequal Link Speeds
- Deploying a Mixed 1Gb/10 Gb Ethernet SAN using Dell Storage PS Series Arrays

The potential advantages in running a mixed-speed (1GbE and 10GbE) PS Series SAN include:

- Not all of the application workloads on a SAN will require storage I/O performance that the 10Gb arrays provide. Thus, SAN administrators will have additional storage tiering flexibility based on array I/O performance.
- The PS Series Group Manager will allow the SAN administrator to still manage both types of arrays within the same SAN group.
- The ability to mix 1Gb and 10Gb arrays supports seamless operational coexistence during migration to a 10Gb SAN.

10.1 Mixed-speed SAN best practices

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The following list summarizes the important SAN design considerations for integrating 10Gb PS Series arrays into existing 1Gb PS Series SANs.

- When integrating 10Gb switches into existing 1Gb switching environment, how the mixed-speed switches are interconnected (split or straight uplink) does not have a significant impact on performance as long as the uplinks are sized appropriately to your workloads.
 - If 1Gb switches are configured as a stack then use the split interconnect pattern as described in <u>Deploying a Mixed 1Gb/10Gb Ethernet SAN using Dell Storage PS Series Arrays</u>.
 - If 1Gb switches are not stacked, then use the straight interconnect pattern
- When connecting 1Gb switches and 10Gb switches together, always be aware of where Rapid Spanning Tree is going to block links to make sure that 10Gb traffic (i.e. PS Series inter-array data flow) never crosses the 1Gb switch.
- Always configure pools and volumes in a way that minimizes impact to I/O performance.
 - Where possible, connect 1Gb hosts only to 1Gb arrays and 10Gb hosts only to 10Gb arrays (except when performing migration tasks). Intermixing speeds may cause oversubscription of ports and lead to high latency or high retransmits.
 - When adding 10Gb arrays, place them in separate pools from 1Gb arrays.

Dell PS Series Configuration Guide

11 Blade server chassis integration

Integrating the PowerEdge M1000e Blade Server Solution (or any third-party blade chassis implementation) requires additional SAN design considerations. Each M1000e can support up to three separate networking "fabrics" that interconnect ports on each blade server to a pair of blade I/O modules within each chassis fabric through an intervening chassis midplane interface. Each fabric is associated with different interfaces on a given blade server as described in Table 37. Each blade server has a "LAN on Motherboard" capability that is mapped to the I/O modules located in the Fabric A I/O modules slots on the M1000e chassis and only supports 1Gb or 10Gb Ethernet networking depending on the blade server model. In addition, each blade server has two "mezzanine" sockets for adding networking options such as 1Gb or 10Gb Ethernet, Infiniband, or Fibre Channel cards. These mezzanine cards are mapped to either the Fabric B or the Fabric C I/O modules.

Figure 20 illustrates the layout of the three fabric I/O modules located on the back of the M1000e chassis.

Table 37 M1000e fabric mapping

	LOM/MLOM	Mezzanine B	Mezzanine C
Fabric	А	В	С

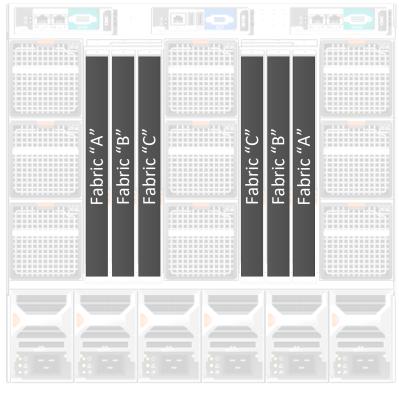


Figure 20 Blade I/O modules and M1000e chassis

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11.1 Designing a SAN using blade chassis I/O modules with arrays

There are three categories of SAN designs for M1000e blade chassis integration:

- Blade IOM switch only (Direct-Attached): Network ports of both the hosts and storage are
 connected to the M1000e blade IOM switches. No Top of Rack (ToR) switches are required. The
 switch interconnect can be a stack or a LAG, and no uplink is required.
- ToR switch only (Pass-Through): Network ports of both the hosts and the storage are connected to external ToR switches. Pass-through IOM are used in place of blade IOM switches in the M1000e blade chassis. The switch interconnect can be a stack, a LAG, or VLT.
- Blade IOM switch with ToR switch (Two-Tier): Host network ports are connected to the M1000e blade IOM switches and the storage network ports are connected to ToR switches. The switch interconnect can be a stack, a LAG, or VLT and should connect the ToR switch to better facilitate inter-array member traffic. An uplink stack, LAG or VLT interconnect from the blade IOM switch tier to the ToR switch tier is also required.

For a detailed description of integrating blade chassis with PS Series arrays read:

- Dell PowerEdge M1000e Blade and EqualLogic PS Series 10 GbE SAN Design Best Practices
- Dell PowerEdge M1000e Blade Enclosure and Dell PS Series SAN Design Best Practices Using Dell S-Series and M-Series Networking (includes best practices for Data Center Bridging environments)

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12 FS Series NAS configuration

This section provides detailed connection diagrams demonstrating how to set up fully connected iSCSI SAN and client LAN connection paths for the FS7500 and FS7600/FS7610 appliances.

Note: It is recommended to keep the client and SAN side networks physically separate and deploy at least two switches on both sides to provide redundancy in the event of a switch failure.

Table 38 lists the basic functional details for each FS Series product.

Table 38 FS Series Models

FS Series model	System components	I/O connections
FS7500	2 x 1U NAS appliance; dual active-active controller configuration 1 x 1U Battery backup power supply unit (BPS)	Client LAN: 8x1GbE per appliance (4 per controller node) iSCSI SAN: 8x1GbE per system (4 per controller node) Controller Interconnect: 8x1GbE per system (4 per controller node)
FS7600	2U NAS appliance with two active/active NAS controllers.	Client LAN: 8x1GbE per appliance (4 per controller node) iSCSI SAN: 8x1GbE per system (4 per controller node)
FS7610	2U NAS appliance with two active/active NAS controllers.	Client LAN: 4x10GbE per appliance (2 per controller node) iSCSI SAN: 4x10GbE per system (2 per controller node)

12.1 FS7500 connection paths

The FS7500 appliance is comprised of two peer system controller nodes. In the required fully cabled configuration, each controller node needs thirteen separate Ethernet cable connections. Thus, a single FS7500 appliance comprised of two controller nodes requires a total of 26 Ethernet connections (four connecting to the client LAN switches and nine connecting to the iSCSI SAN fabric switches.)

Figure 21 shows the client LAN connection paths.

Note: While it is possible to operate an FS7500 appliance in a partially cabled configuration, this configuration is not supported by Dell EMC. Use a fully cabled configuration in a production environment. Also, provision the required switch port count on the iSCSI SAN and client LAN sides of the system to support a fully connected configuration.

Connection Paths for FS7500 Client LAN

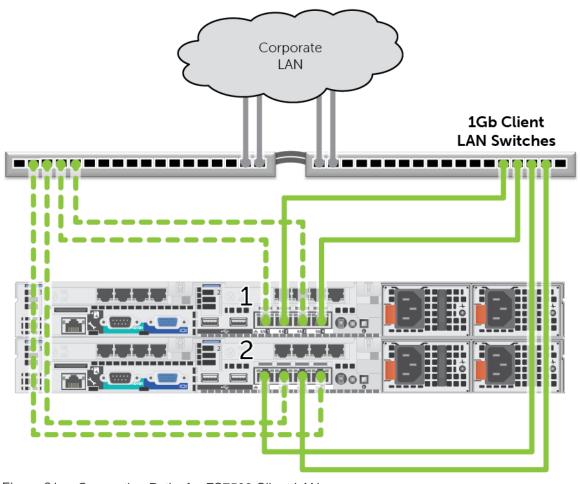


Figure 21 Connection Paths for FS7500 Client LAN

Figure 22 below shows the iSCSI SAN, IPMI, and node interconnect paths. Pay careful attention to how the controller ports alternate between redundant switch paths.

Note: With the exception of the IPMI connection paths, corresponding ports on each controller node must connect to the same SAN switch. This connection pattern is shown in Figure 22.

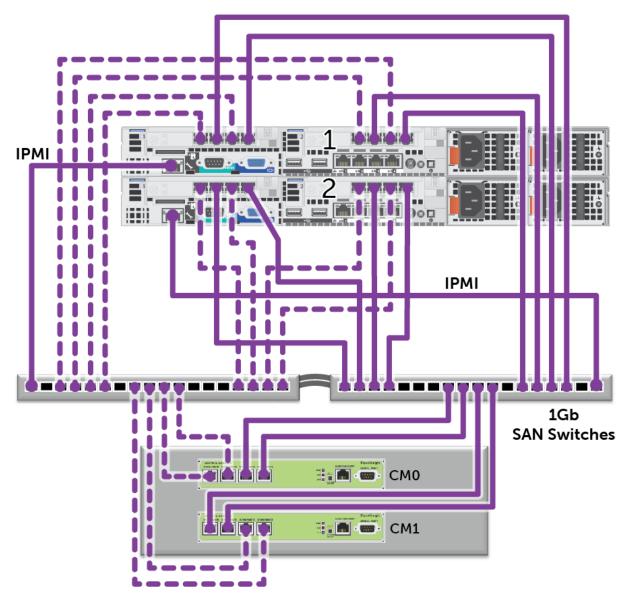


Figure 22 Connection Paths for FS7500 iSCSI SAN, IPMI and Controller Interconnect

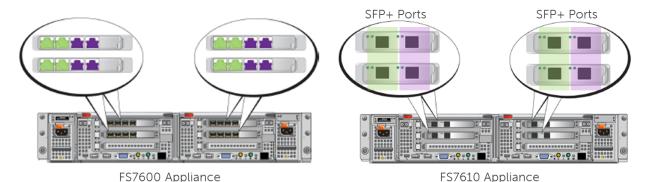
The inter-switch connection sizing guidelines provided in section 8.2.2 also apply to FS7500 SAN design. The FS7500 mirrors the write cache between controller nodes. To accomplish this, all write operations are transmitted across the controller interconnect. Thus, it is very important to follow the connection pattern shown in Figure 22 to ensure corresponding ports are connected to the same switch. This connection pattern prevents controller interconnect traffic from crossing the inter-switch connection.

12.2 FS7600/FS7610 connection paths

The PS Series NAS appliances require the following networks:

- Client network: Used for client access to the NFS exports and CIFS shares hosted by the NAS cluster.
- SAN/internal network: Used for internal communication between the controllers and communication between the controllers and the PS Series SAN. The SAN and Internal networks use the same set of switches.

It is best to keep the client and SAN side networks physically separate and deploy two switches on both sides to protect against a switch failure.



 Network
 Network Usage
 Ethernet Port Numbers

 Client
 The Client network allows the clients (workstations, PCs, etc.) to
 Left port in all

Client network The Client network allows the clients (workstations, PCs, etc.) to access network shares.

Left port in all NICs

SAN /
Internal networks The SAN network connections allow communication between the NAS cluster and the PS Series group (SAN).
The SAN and internal networks use two different IP address ranges, but they exist on the same switch or VLAN.
The internal network connections allow communication between the two controllers.

Figure 23 FS7600 and FS7610 networks

See figures below for network connections.

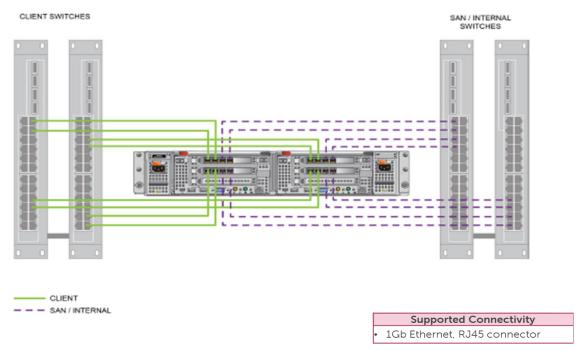


Figure 24 FS7600 network

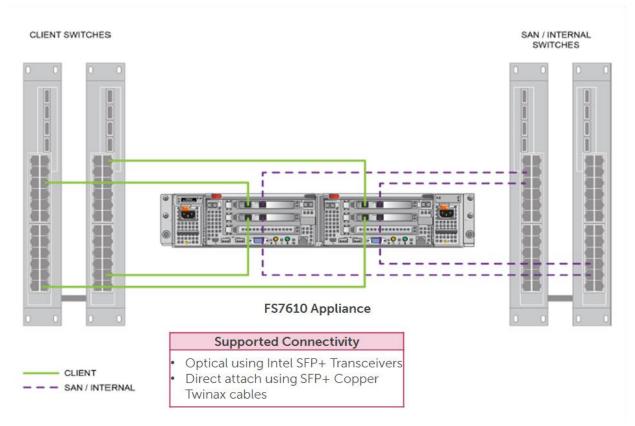


Figure 25 FS7610 network

Installation/expansion

- If installing FS7500/FS76x0 into an existing PS Series SAN, verify the existing LAN and SAN networks meet the minimum requirements.
- All NAS appliances in a NAS cluster must be of the same speed. Appliances with different connectivity cannot be mixed in a NAS cluster.
 - An FS7500 cluster can be expanded by using another FS7500 or an FS7600 but **not** an FS7610.
- Do not block IPv6 traffic on the SAN internal switches when utilizing FS Appliances. The FS76x0
 uses IPv6 for internal communication and setup only; not for user data. Client communication occurs
 over IPv4.

Network

• The two iDRAC IPMI ports on the FS7500 are 100Mb. The SAN switches must be able to support 100Mb in addition to 1Gbps speed.

DØLLEMC

13 Data Center Bridging (DCB)

The enhancement to the Ethernet Specifications (IEEE 802.3 specifications) called Data Center Bridging (DCB) enables bandwidth allocation and lossless behavior for storage traffic when the same physical network infrastructure is shared between storage and other traffic.

The network is the fundamental resource that connects the assorted devices together to form the datacenter Ethernet infrastructure. These devices include the server hardware (along with the operating system and the applications that run on the host) and the storage systems that host application data. Sharing this Ethernet infrastructure with multiple traffic types (LAN and SAN) requires a fairness mechanism to provide bandwidth allocation and flow control for each type of traffic. Without such a mechanism, the Local Area Network (LAN) traffic and the Storage Area Network (SAN) traffic would have to be separated onto their own dedicated networks to ensure consistent performance and reliability.

When the SAN and LAN networks are shared, all traffic is equal, unless Quality of Service (QoS) and/or Class of Service (CoS) is used.

Note: Traditional or Non-DCB QoS (IEEE 802.1p) is not supported for PS Series implementations.

In a shared network environment, LAN and SAN traffic can impact each other and QoS may not solve this because of QoS implementation differences with vendors. These different implementations include:

- · Number of Queues managed
- Relative priorities between queues
- · Bandwidth reserved per queue
- Congestion management methods

Also, traditional QoS lacks selective flow control for each traffic type. Regular IEEE 802.3 PAUSE will pause the entire link and not selectively pause LAN or SAN traffic. This flow control ability is important for reducing congestion on switches and to enable fair sharing of resources between traffic types.

Other methods of network sharing:

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- VLANs offer port-level security and segregation, but do not provide guaranteed bandwidth or QoS.
- NIC partitioning (NPAR) manages traffic within the host. Once the network data exits the NIC to the switch, any QoS/bandwidth management enforced by NPAR is not honored by the switch.

Note: It is not recommended to share network infrastructure without DCB. DCB is the only recommended method of converging SAN and LAN in a PS Series Storage environment.

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13.1 DCB overview

DCB is a collection of Standards designed to improve networking and management in the data center that enables iSCSI SANs or FCoE SANs or both to converge with regular server LAN traffic on the same physical infrastructure. DCB aims to increase operational efficiency, constrain costs, and ease network management.

There are primarily three progressive versions of DCB:

- Cisco, Intel, Nuova (CIN) DCBX
- Converged Enhanced Ethernet (CEE) DCBX or baseline DCBX
- Institute of Electrical and Electronic Engineers (IEEE) DCB.

DCB technologies based on standards include:

- PFC Priority based Flow Control (802.1Qbb)
- ETS Enhanced Transmission Selection (802.1Qaz)
- CN Congestion Notification (802.1Qau)
- DCBx Data Center Bridging Capability eXchange
 - Support for iSCSI application protocol priority with DCBX (also known as iSCSI TLV)

Note: DCB uses 10Gb and higher Ethernet only.

I/O convergence using DCB for PS Series iSCSI storage is the direction for "Converged iSCSI" in a lossless Ethernet environment.

13.2 DCB requirements for PS Series arrays

It is required that all devices in the PS Series SAN support DCB for iSCSI when this functionality is enabled. If any device in the SAN does not support DCB, then DCB needs to be disabled at the switches for the entire SAN. Once all devices in the SAN are DCB compliant, then DCB can be re-enabled. Switches and server CNAs/NICs that are designated as DCB Supported in the Dell Storage Compatibility Matrix have been fully validated by Dell to ensure compatibility for PS Series SANs.

The minimum requirements for switches and server-based CNA/NICs to support an end-to-end DCB solution with PS Series arrays are:

- Data Center Bridging Exchange (DCBx): DCB protocol that performs discovery, configuration, and mismatch resolution using Link Layer Discovery Protocol (LLDP)
- Application Priority (iSCSI TLV): Switches must support configuration of a priority value for iSCSI
 protocol and advertisement to peer ports. Server NICs/CNAs: Must support iSCSI protocol in
 application priority (learned from the switch) and must support tagging outgoing iSCSI frames with
 this priority.
- ETS: Requires a dedicated traffic class (TC) or priority group (PG) for iSCSI priority
- PFC: Requires enabling PFC (no drop or lossless behavior) for iSCSI priority
- Switches: Configure dedicated TC/PG for iSCSI priority with allocated bandwidth and PFC enabled for iSCSI priority
- Server NICs/CNAs: Adhere to TC/PG mapping for iSCSI priority and PFC for iSCSI priority (learned from the switch)

Designing a converged network deployment with components that have no DCB support or partial DCB support is not recommended for end-to-end converged I/O. Configurations with components that have no DCB support or partial DCB support may result in loss of expected functionality or may be functional without meeting the converged network objectives.

Note: It is not recommended to share network infrastructure with DCB and non-DCB components.

For a complete list of components that support the DCB standards, see the *Dell EMC Storage Compatibility Matrix*.

Note: It is important to verify that all components in the SAN are listed in the Dell Storage Compatibility Matrix as DCB Supported, or that the components support all PS Series requirements for DCB.

See the following links to DCB papers:

- Dell PS Series DCB Configuration Best Practices
- Dell PowerEdge M1000e Blade Enclosure and Dell PS Series SAN Design Best Practices Using Dell S-Series and M-Series Networking

13.3 VLANs for iSCSI

A non-default VLAN is required for operating prioritized lossless iSCSI traffic in a DCB enabled Ethernet infrastructure. Switch ports that are based on the IEEE 802.1Q VLAN specification forward frames in the default or native VLAN without tags (untagged frames). Normally these ports also receive frames in the default or native VLAN as untagged. Such a port is typically termed a "tagged port" or "trunk port" and all non-default VLAN frames are forwarded with the tags intact. Since the DCB priority information (PCP value) is encoded in the VLAN tag, this information will be lost if an end-device sends iSCSI traffic in a default or native VLAN and the switch receives it in a tagged or trunk port. As a result, DCB prioritization and traffic classification will be lost.

All devices in the iSCSI data path must have the same VLAN ID configured on the respective ports participating in the iSCSI network to ensure proper functioning. These devices include the server iSCSI NIC/CNA ports, Ps Series arrays, and all switches on the iSCSI SAN.

Note: The VLAN ID for iSCSI can be set in the PS Series Group Manager interface or the storage array CLI. When DCB is enabled on the switch, it is necessary to configure a non-default VLAN on the array, switch, and all host ports that are part of the PS Series SAN. VLAN IDs 0 and 1 are not recommended, because these may be the default or reserved VLAN for some switches, and as such, may forward frames untagged (e.g. no VLAN tagging). VLAN tagging is required to fully support DCB.

For more information on the DCB requirements and configuration guidelines for PS Series, to ensure that DCB is properly enabled and/or disabled across all devices, and to assist with identifying and resolving basic DCB configuration issues in the PS Series SAN, see the following white paper: *Dell PS Series DCB Configuration Best Practices*

For a comprehensive understanding of integrating an M1000e Blade Chassis in a Full End-to-End Data Center Bridging environment, see the following white paper: *Dell PowerEdge M1000e Blade Enclosure and Dell PS Series SAN Design Best Practices Using Dell S-Series and M-Series Networking*

A Network ports and protocols

PS Series groups use a number of TCP and UDP protocols for group management, I/O operations, and internal communication. If you have switches or routers set to block these protocols, you may need to unblock them to allow management or I/O operations to work correctly. The required and optional protocols are listed in the following sections.

A.1 Required ports and protocols

Table 39 lists the ports and protocols required for operating a PS Series iSCSI SAN.

Table 39 Required ports and protocols

i able 39	Required ports and protocols				
Туре	Port	Protocol	Access		
iSCSI	iscsi				
ТСР	3260	iSCSI	To the group IP address and all individual member IP addresses		
PS Series	PS Series Internal				
UDP	161	SNMP	Management operations		
ТСР	9876	Internal	iSCSI intra-system control		
ТСР	25555	Internal	Group communication		
ТСР	20002	Internal	Event logging		

A.2 Optional ports and protocols

Table 40 lists the optional ports and protocols used for management and alerts. They are not required for correct array operation, however blocking a port may reduce specific functionality.

Table 40 Optional ports and protocols

Туре	Port	Protocol	Access	
CLI Mana	gement			
TCP	23	Telnet	To group IP address	
TCP	22	SSH	To group IP address	
Web Bas	ed Manage	ement		
TCP	80	НТТР	To group IP address	
TCP	3002	GUI communication	To group IP address	
TCP	3003	GUI communication (encrypted)	To group IP address	
SNMP	SNMP			

Туре	Port	Protocol	Access		
UDP	161	SNMP	To and from group IP address		
Syslog					
UDP	514	Syslog	From group IP address		
PS Series	s Diagnost	ics			
TCP	21	FTP	Software update and diagnostic procedures; to all individual member IP addresses		
TCP	25	SMTP	E-mail and diagnostic notifications; from all individual member IP addresses to the configured SMTP server		
NTP	NTP				
UDP	123	NTP	To external Network Time Protocol (NTP) sources for time-of-day clock synchronization		

B Upgrade paths for PS Series arrays

Table 41 PS Series upgrade paths

Currently shipping arrays	Latest available conversion model	1Gb to 10Gb conversion availability	Drive upgrades availability (to half populated arrays)
PS6610	None	N/A	Yes
PS4210	None	N/A	Yes
PS6210	None	N/A	Yes
PS-M4110	None	N/A	Yes

End of sales life arrays	Latest available conversion model	1Gb to 10Gb conversion availability	Drive upgrades availability (to half populated arrays)
PS-50 through PS3000	None	N/A	None
PS4000	None	N/A	Yes
PS4100	None	N/A	Yes
PS4110	None	N/A	Yes
PS5000	None	N/A	Yes
PS5500	None	N/A	Yes
PS6000	None	N/A	Yes
PS6010	None	N/A	Yes
PS6100	None	N/A	Yes
PS6110	None	N/A	Yes
PS6500	None	N/A	Yes
PS6510	None	N/A	Yes