



# Statement of Volatility – Dell PowerEdge M630/FC630

Dell PowerEdge M630/FC630 contains both volatile and non-volatile (NV) components. Volatile components lose their data immediately upon removal of power from the component. Non-volatile components continue to retain their data even after the power has been removed from the component. Components chosen as user-definable configuration options (those not soldered to the motherboard) are not included in the Statement of Volatility. Configuration option information (pertinent to options such as microprocessors, remote access controllers, and storage controllers) is available by component separately. The following NV components are present in the PowerEdge M630/FC630 server.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size
<b>Planer</b>				
PCH Internal CMOS RAM	Volatile	1	U_WBGA	256 Bytes
BIOS SPI Flash	Non-Volatile	1	U_SPI_BIOS1	16 MB
iDRAC SPI Flash	Non-Volatile	1	U_IDRAC_SPI1	4 MB
BMC EMMC	Non-Volatile	1	U_EMMC1	4 GB
BMC Memory	Volatile	1	U_IDRAC_MEM	2 Gb
CPU Vcore and Memory regulator	Non-Volatile	6	PU51, PU66, PU71, PU58, PU76, PU81	
System CPLD	Non-Volatile	1	U_CPLD	2210 logic elements 1700 macrocells 272 IOs
System Memory	Volatile	Up to 12 per CPU	CPU1: DIMMA1~DIMMA12, CPU2: DIMMB1~DIMMB12	Up to 32GB per DIMM
<b>M630 2x2.5" SATA Backplane</b>				
SEP internal flash	Non-Volatile	1	U_SEP1	Flash: 32KB EEPROM: 16KB
<b>M630 2x2.5" SAS Backplane</b>				
SEP internal flash	Non-Volatile	1	U_SEP1	Flash: 32KB EEPROM: 16KB

<b>M630 4x1.8" SATA Backplane</b>				
SEP internal flash	Non-Volatile	1	U_SEP1	Flash: 32KB EEPROM: 16KB
<b>M630 4x1.8" SAS Backplane</b>				
SEP internal flash	Non-Volatile	1	U_SEP1	Flash: 32KB EEPROM: 16KB
<b>M630 2x2.5" PCIe SSD Backplane</b>				
SEP internal flash	Non-Volatile	1	U_SEP1	Flash: 32KB EEPROM: 16KB
<b>FC630 2x2.5" SATA Backplane</b>				
SEP internal flash	Non-Volatile	1	U_SEP1	Flash: 32KB EEPROM: 16KB
<b>FC630 2x2.5" SAS Backplane</b>				
SEP internal flash	Non-Volatile	1	U_SEP1	Flash: 32KB EEPROM: 16KB
<b>FC630 8x1.8" SATA Backplane</b>				
SEP internal flash	Non-Volatile	1	U_SEP1	Flash: 32KB EEPROM: 16KB
<b>FC630 8x1.8" SAS Backplane</b>				
SEP internal flash	Non-Volatile	1	U_SEP1	Flash: 32KB EEPROM: 16KB
<b>FC630 2x2.5" PCIe SSD Backplane</b>				
SEP internal flash	Non-Volatile	1	U_SEP1	Flash: 32KB EEPROM: 16KB
<b>IDSDM Card</b>				

IDSDM SPI flash	Non-Volatile	1	U_SPI	8Mb
rSPI flash	Non-Volatile	1	U_RSPI	4MB
<b>rSPI Card</b>				
rSPI flash	Non-Volatile	1	U_RSPI	4MB


Item	Type (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	Purpose? (e.g. boot code)
<b>Planer</b>			
PCH Internal CMOS RAM	Battery-backed CMOS RAM	Yes	Real-time clock and BIOS configuration settings
BIOS SPI Flash	SPI Flash	Yes	Boot code, system configuration information, UEFI environment, Flash Disceptor, ME
iDRAC SPI Flash	SPI Flash	Yes	iDRAC Uboot (bootloader), server management persistent store (i.e. iDRAC MAC Address, iDRAC boot variables), lifecycle log cache, virtual planar FRU and EPPID, rac log, System Event Log,
BMC EMMC	eMMC NAND Flash	Yes	Operational iDRAC FW, Lifecycle Controller (LC) USC partition, LC service diags, LC OS drivers, USC firmware
BMC Memory	RAM	Yes	iDRAC RAM.
CPU Vcore and Memory regulator	NVRAM	No	CPU Vcore and Memory controller
System CPLD	Flash	Yes	Power sequence controller and detection.

Item	Type (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	Purpose? (e.g. boot code)
System Memory	RAM	Yes	System OS RAM
<b>M630 2x2.5" SATA Backplane</b>			
SEP internal flash	Integrated Flash+EEPROM	Yes	Backplane controller
<b>M630 2x2.5" SAS Backplane</b>			
SEP internal flash	Integrated Flash+EEPROM	Yes	Backplane controller
<b>M630 4x1.8" SATA Backplane</b>			
SEP internal flash	Integrated Flash+EEPROM	Yes	Backplane controller
<b>M630 4x1.8" SAS Backplane</b>			
SEP internal flash	Integrated Flash+EEPROM	Yes	Backplane controller
<b>M630 2x2.5" PCIe SSD Backplane</b>			
SEP internal flash	Integrated Flash+EEPROM	Yes	Backplane controller
<b>FC630 2x2.5" SATA Backplane</b>			
SEP internal flash	Integrated Flash+EEPROM	Yes	Backplane controller
<b>FC630 2x2.5" SAS Backplane</b>			
SEP internal flash	Integrated Flash+EEPROM	Yes	Backplane controller
<b>FC630 8x1.8" SATA Backplane</b>			
SEP internal flash	Integrated Flash+EEPROM	Yes	Backplane controller
<b>FC630 8x1.8" SAS</b>			

Item	Type (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	Purpose? (e.g. boot code)
<b>Backplane</b>			
SEP internal flash	Integrated Flash+EEPROM	Yes	Backplane controller
<b>FC630 2x2.5" PCIe SSD Backplane</b>			
SEP internal flash	Integrated Flash+EEPROM	Yes	Backplane controller
<b>IDSDM Card</b>			
IDSDM SPI flash	SPI Flash	Yes	Source code for IDS DM controller
rSPI flash	SPI Flash	No	rSPI function
<b>rSPI Card</b>			
rSPI flash	SPI Flash	No	rSPI function

Item	How is data input to this memory?	How is this memory write protected?
<b>Planer</b>		
PCH Internal CMOS RAM	BIOS	NVRAM_CLR jumper
BIOS SPI Flash	SPI interface via PCH	Software write protected
iDRAC SPI Flash	SPI interface via iDRAC	Software write protected
BMC EMMC	NAND Flash interface via iDRAC	Embedded FW write protected
BMC Memory	Memory interface via iDRAC	Not applicable
CPU Vcore and Memory regulator	OTP(one time programmable)	Not accessible
System CPLD	System OS RAM	System OS

Item	How is data input to this memory?	How is this memory write protected?
System Memory	System OS	OS Control
<b>M630 2x2.5" SATA Backplane</b>		
SEP internal flash	Firmware + FRU	I2C interface via iDRAC
<b>M630 2x2.5" SAS Backplane</b>		
SEP internal flash	Firmware + FRU	I2C interface via iDRAC
<b>M630 4x1.8" SATA Backplane</b>		
SEP internal flash	Firmware + FRU	I2C interface via iDRAC
<b>M630 4x1.8" SAS Backplane</b>		
SEP internal flash	Firmware + FRU	I2C interface via iDRAC
<b>M630 2x2.5" PCIe SSD Backplane</b>		
SEP internal flash	Firmware + FRU	I2C interface via iDRAC
<b>FC630 2x2.5" SATA Backplane</b>		
SEP internal flash	Firmware + FRU	I2C interface via iDRAC
<b>FC630 2x2.5" SAS Backplane</b>		
SEP internal flash	Firmware + FRU	I2C interface via iDRAC
<b>FC630 8x1.8" SATA Backplane</b>		
SEP internal flash	Firmware + FRU	I2C interface via iDRAC
<b>FC630 8x1.8" SAS Backplane</b>		
SEP internal flash	Firmware + FRU	I2C interface via iDRAC
<b>FC630 2x2.5" PCIe SSD Backplane</b>		
SEP internal flash	Firmware + FRU	I2C interface via iDRAC
<b>IDSDM Card</b>		
IDSDM SPI flash	I2C interface via iDRAC	Software write protected

 **NOTE:** For any information that you may need, direct your questions to your Dell Marketing contact.

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