BIOS Version 2.5.5 for Dell PowerEdge Servers: R730XL, R730xdXL, and R630XL

Release Notes

Release Notes

BIOS

Basic Input / Output System (BIOS) facilitate the hardware initialization process and transition control to the operating system.

Current Version

2.5.5

Release Date

August 2017

Previous Version

2.5.5

Importance

RECOMMENDED: Dell recommends applying this update during your next scheduled update cycle. The update contains feature enhancements or changes that will help keep your system software current and compatible with other system modules (firmware, BIOS, drivers, and software).

Platform(s) affected

PowerEdge R730XL, R730XDXL, and R630XL servers.

What's New

- Updated the Intel Processor and Memory Reference Code (MRC) to PLR11.
- Updated the Intel Xeon Processor E5-2600 v4 Product Family Processor Microcode to version 0x21.
- Updated the Intel Xeon Processor E5-2600 v3 Product Family Processor Microcode to version 0x3A.
- Improved the PCIe Slot CTLE settings for the E5-2600 v4 processors.
- Secure MOR change to support the Windows 2016 R3 server version.
- Added support for the Redfish management interfaces.
- Added improvements to Non-Volatile Memory Express (NVMe).
- Added improvements to the Secure Boot Custom Policy settings.
- Added improvements to the S130 storage.
- Added support for mixed Quad Rank and Dual Rank LRDIMMs.
- Added the I/O Snoop HoldOff Response BIOS setup option in the Integrated Devices menu.

Fixes

- Added workaround to address the uncorrectable errors on RDIMMs with specific vendor or the rev Register Clock Driver (RCD).
- Updated ECRC (end-to-end CRC checking) to prevent the OS from changing the BIOS settings.
- Fixed CPU machine check issue when the Write Data Cyclic Redundancy Check (CRC) is enabled.

- Removed BIOS power capping when in performance mode. This prevents the Node Manager power capping SCI from triggering.

- A PCI resource handling issue on the PowerEdge R630 server.

- Applied Intel Xeon Processor E5-2600 v3 memory CLK/RCD Memory Reference Code (MRC) workaround on Intel Xeon Processor E5-2600 v4.

Important notes
None for this release.

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History of previous Release Notes

Version
2.2.5

Release Date
September 2016

What's New

- Updated the Intel Processor and Memory Reference Code to PLR4.
- Updated the Intel Xeon Processor E5-2600 v4 Product Family Processor Microcode to version 0x1E.
- Updated the Intel Xeon Processor E5-2600 v3 Product Family Processor Microcode to version 0x38.
- Updated the Intel Trusted Execution Technology (Intel TXT) BIOS and SINIT Authenticated Code Module (ACM) to version 3.1.0.
- The Intel TXT feature is supported with Trusted Platform Module (TPM) version 2.0.
- Updated TPM version 2.0 support.
- Updated the integrated Dell Remote Access Controller (iDRAC) Human Interface Infrastructure (HII) to version 2.40.40.05.
- Added the new slot bifurcation BIOS Setup options.
- Updated text in the BIOS Setup menu help content.
- Changed the default setting of BIOS Setup option In-System-Characterization to Disabled.

Fixes

- The boot order may change after updating the BIOS version.

- The cause of system internal error (IERR) is not getting logged..

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