

# Statement of Volatility – Dell EMC PowerEdge MX750c

The Statement of Volatility provides you the information related to volatile and non-volatile components of different configurations of Dell EMC PowerEdge servers.

Volatile components lose their data when power cord is removed from the system, whereas, non-volatile components continue to retain their data when the power has been removed from the component.

The following table provides information of different configurations of the PowerEdge MX750c.

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal operation?	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write-protected?	How is memory cleared?
<b>Planar</b>										
PCH Internal CMOS RAM	Non-Volatile	1	U_PCH	256 Bytes	Battery-backed CMOS RAM	No	Real-time clock and BIOS configuration settings	BIOS	N/A – BIOS only control	Perform the following steps: 1) Set NVRAM_CLR jumper to clear BIOS configuration settings at boot and reboot system; 2) AC power off system, remove coin cell battery for 30 seconds, replace battery and power back on; 3) restore default configuration in F2 system setup menu.
BIOS Password (part of PCH internal CMOS RAM)				16 bytes (out of 256 bytes used for PCH Internal CMOS RAM)		Yes	Password to change BIOS settings	Keyboard	N/A – BIOS only control	1) Place shunt on J_PSWD_NVRAM jumper pins 4 and 6. 2) AC power off is required after placing the shunt.

Primary BIOS SPIFlash	Non- Volatile	1	U4027	32MB	SPI Flash	No	Boot code	SPI interface via PCH	Software write protected	Not possible with any utilities or applications and system is not functional if corrupted/removed.
CPU Vcore Regulators	Non- Volatile	2	PAAU1, PBAU1	32 KB	ROM	No	Operational parameters	Programmed at factory via I2C	No write protect	Not user clearable
Vmem Regulators	Non- Volatile	2	PAEU1, PBEU1	32 KB	ROM	No	Operational parameters	Programmed at factory via I2C	No write protect	Not user clearable
System CPLD RAM	Volatile	1	U_CPLD1	432Kb	RAM	No	Not utilized	Not utilized	Not accessible	Not accessible
System CPLD FLASH	Non- Volatile	1	U_CPLD1	448Kb	FLASH	No	Power on System Firmware	Firmware update	BIOS Security Protocols	Not user clearable
System Memory: RDIMM and LRDIMM	Volatile	Up to 16 per CPU	CPU<2:1>_CH<H:A>_D<1:0>	Up to 64GB per RDIMM UP to 128GB Per LRDIMM	DRAM	Yes	System OS RAM	System OS	OS Control	Reboot or power down system

System Memory: BPS	Non- Volatile	Up to 8 per CPUs 1 and 2 (16 total in system)	BPS1: CPU<2:1>_CH<H,F,D,B>_D<0>  BPS2: CPU<2:1>_CH<D>_D<0>  BPS3: CPU<2:1>_CH<A>_D<1>  BPS4: CPU<2:1>_CH<G,E,C,A>_D<1>  BPS5: CPU<2:1>_CH<H:A>_D<1>  BPS6: CPU<2:1>_CH<F,B>_D<0>	128GB, 256GB, 512GB per BPS	Flash – BPS	No	Data integrity	When system initiates a Save (AC loss, shutdown, etc.), BPS controller will transfer data from DRAM to Flash	Neither system nor OS can access the flash, only a system initiated Save will trigger the BPS controller to transfer data from DRAM to Flash	Using BIOS menu option, select "Persistent Memory"
Internal USB Key	Non- Volatile	Up to 1	J_USB_INT	Varies (not factory installed)	Flash	Yes	General purpose USB key drive	USB interface via PCH. Accessed via system OS	No write protect	Can be cleared insystem OS
CPU	Volatile	1 or 2	CPU1 /CPU2	Various	Cache + registers	Yes	Processor cache + registers	Various	Various	Remove A/C
PIROM	Non- Volatile	1 or 2	CPU1 /CPU2	128 Bytes	EEPROM	No	Processor info + scratchpad	SMBus interface to iDRAC	128 bytes protected by Intel/128 bytes not protected	Not user clearable

Recovery BIOS SPI	Non- Volatile	1	U4030	4MB	SPI Flash	No	Recovery image	SPI interface via iDRAC	No write protect	Not user clearable
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iDRAC SPI Flash	Non-Volatile	1	U59	4 MB	SPI Flash	No	iDRAC Uboot (bootloader)	SPI interface via iDRAC	Embedded iDRAC subsystem firmware actively controls sub area based write protection as needed.	Not completely user clearable; however, user data, lifecycle log and archive, SEL, FW image repository can be cleared via Delete Configuration and Retire System, accessible in Lifecycle Controller interface
BMC eMMC	Non-Volatile	1	U4031	8GB, 16GB	eMMC NAND Flash	No	Operational iDRAC FW, Lifecycle Controller (LC) USC partition, LC service diags, LC OS drivers, USC firmware	NAND Flash interface via iDRAC	Embedded FW write protected	Not completely user clearable; however, user data, lifecycle log and archive, SEL, FW image repository can be cleared via Delete Configuration and Retire System, accessible in Lifecycle Controller interface
iDRAC DDR	Volatile	1	U4033	4Gb, 8Gb	DRAM	No	iDRAC local memory	iDRAC Firmware	No write protect	Remove A/C
iDRAC	Volatile	1	U_IDRAC1	For CPU: 128KB + Registers Co-proc: 64Kb + Registers	Cache + registers	No	Processor cache + registers	iDRAC Firmware	No write protect	Remove A/C

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**6x2.5" Universal BP**

SEP internal flash	Non-Volatile	1	U47	Flash:512KB Data SRAM : 256KB Battery Powered Storage SRAM : 64B	Integrated Flash+Data SRAM+Battery Powered Storage SRAM	No	Firmware + FRU	I2C interface via iDRAC	Program write protect bit	Not user clearable
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**4x2.5" Universal BP**

SEP internal flash	Non-Volatile	1	U47	Flash:512KB Data SRAM : 256KB Battery Powered Storage SRAM : 64B	Integrated Flash+Data SRAM+Battery Powered Storage SRAM	No	Firmware + FRU	I2C interface via iDRAC	Program write protect bit	Not user clearable
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Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write- protected?	How is memory cleared?
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**6x2.5" SAS/SATA BP**

SEP internal flash	Non-Volatile	1	U47	Flash:512KB Data SRAM : 256KB Battery Powered Storage SRAM : 64B	Integrated Flash+Data SRAM+Battery Powered Storage SRAM	No	Firmware + FRU	I2C interface via iDRAC	Program write protect bit	Not user clearable
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Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write-protected?	How is memory cleared?
<b>H755 MX</b>										
NVSRAM	Non-volatile	1	U1087	1Mb	NVSRAM	No	Configuration data	ROC writes configuration data to NVSRAM	No write protect Not visible to Host Processor	Not user clearable
FRU	Non-volatile	1	U1019	2Kb	FRU	No	Card manufacturing information	Programmed at ICT during production	No write protect	Not user clearable
SPD	Non-volatile	1	U22	2Kb	SPD	No	Memory configuration data	Pre-programmed before assembly	No write protect Not visible to Host Processor	Not user clearable
Flash	Non-volatile	1	U1086	128Mb	Flash	No	Card firmware	Pre-programmed before assembly. Can be updated using Dell/Broadcom tools	No write protect Not visible to Host Processor	Not user clearable



Backup Flash	Non-volatile	1	U1100	512Gb	Backup Flash	No	Holds cache data during power loss	ROC backs up DDR data to this device incase of a power failure	No write protect Not visible to Host Processor	Flash can be cleared by powering up the card and allowing the controller to flush the contents to VDs. If the VDs are no longer available, cache can be cleared by going into controller bios and selecting Discard Preserved Cache.
SDRAM	Volatile	9	U1077-U1085	8GB	SDRAM	No	Cache for HDDI/O	ROC writes to this memory -using it as cache for data IO to HDDs	No write protect Not visible to Host Processor	Cache can be cleared by powering off the card

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<b>H745P MX</b>										
NVSRAM	Non-volatile	1	U1087	1Mb	NVSRAM	No	Configuration data	ROC writes configuration data to NVSRAM	No write protect Not visible to Host Processor	Not user clearable
FRU	Non-volatile	1	U1019	64Kb	FRU	No	Card manufacturing information	Programmed at ICT during production.	No write protect	Not user clearable
1-Wire EEPROM	Non-volatile	1	U1004	1Kb	1-Wire EEPROM	No	Holds default controller properties/settings	ROC writes data to this memory	No write protect Not visible to Host Processor	Not user clearable
Serial Boot ROM	Non-volatile	1	U1020	4KB	Serial Boot ROM	No	Bootloader	Pre-programmed before assembly	No write protect Not visible to Host Processor	Not user clearable
SPD	Non-volatile	1	U22	2Kb	SPD	No	Memory configuration data	Pre-programmed before assembly	No write protect Not visible to Host Processor	Not user clearable


Flash	Non-volatile	1	U1086	128Mb	Flash	No	Card firmware	Pre- programmed before assembly. Can be updated using Dell/Broadcom tools	No write protect Not visible to Host Processor	Not user clearable
Backup Flash	Non-volatile	1	U1100	64Gb	Backup Flash	No	Holds cachedata duringpower loss	ROC backs up DDR data to this device incase of a power failure	No write protect Not visible to Host Processor	Flash can be cleared by powering up the card and allowing the controller to flush the contents toVDs. If the VDs are no longer available, cache can be clearedby going into controller bios and selecting Discard Preserved Cache.
SDRAM	Volatile	9	U1077-U1085	8GB	SDRAM	No	Cache for HDDI/O	ROC writes to this memory -using it as cache for data IO to HDDs	No write protect Not visible to Host Processor	Cache can be clearedby powering off the card

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<b>HBA350i MX</b>										
FRU	Non-volatile	1	U1019	2Kb	FRU	No	Card manufacturing information	Programmed at ICT during production	No write protect	Not user clearable
Serial Boot ROM	Non-volatile	1	U1020	64Kb	Serial Boot ROM	No	Bootloader	Pre-programmed before assembly	No write protect Not visible to Host Processor	Not user clearable
Flash	Non-volatile	1	U1031	128Mb	Flash	No	Card firmware	Pre-programmed before assembly. Can be updated using Dell/Broadcom tools	No write protect Not visible to Host Processor	Not user clearable

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<b>HBA330 MMZ</b>										
FRU	Non-volatile	1	U1019	64Kb	FRU	No	Card manufacturing information	Programmed at ICT during production	no write protect	Not user clearable
Serial Boot ROM	Non-volatile	1	U1020	64Kb	Serial Boot ROM	No	Bootloader	Pre-programmed before assembly	no write protect Not visible to Host Processor	Not user clearable
Flash	Non-volatile	1	U1031	128Mb	Flash	No	Card firmware	Pre-programmed before assembly. Can be updated using Dell/Broadcom tools	no write protect Not visible to Host Processor	Not user clearable

Item	Non-Volatile or Volatile	Quantity	Reference Designator	Size of memory	Type of memory (e.g. Flash PROM, EEPROM)	Can user programs or operating system write data to it during normal	What is the Purpose? (e.g. boot code)	How is data written to this memory?	How is memory write-protected?	How is memory cleared?
<b>TPM</b>										
Trusted Platform Module (TPM)	Non-Volatile	1	U_TPM	Up to 64 Bytes	SPI Flash	Yes	Storage of encryption keys	Using TPM Enabled operating systems	SW write protected	F2 Setup option

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<b>IDSDM</b>										
iDSDM (uSD1, uSD2)	Non-volatile	2	J_IDSDM_SD1, J_IDSDM_SD2	16GB, 32GB, 64GB	NAND Flash	Yes	Provides mass storage	Device resides in host domain; they are exposed to the user via an internally connected, non-removable USB mass storage device	physical write protect switch on ACE card	(1) Card may be physically removed and destroyed or cleared via standard means on a separate computer OR (2) User has access to the card in the host domain and may clear it manually
SPI Flash	Non-Volatile	1	U_SPI	1MB	SPI Flash	SPI flash is only indirectly connected to iDRAC. iDRAC can read any address in the SPI flash, but may only write the primary firmware storage area as part of a firmware update procedure	Boot firmware storage, configuration and state data for iDSDM	User can initiate a firmware update of the iDSDM device.	There is no mechanism provided to iDRAC to write any SPI NOR area outside of the primary iDSDM firmware region	iDRAC may issue a clear command to erase all contents of the SPI NOR, but doing this will leave the iDSDM non-functional

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<b>BOSS_M.2 SATA/PCI-E riser card</b>										
SPI FLASH	Non-Volatile	1	U17	1024KB	FLASH EEPROM	No	Boot code, FW	By programming the image via firmware update process	N/A	Use Flash tool, type "go.nsh w y"
TFRU	Non-Volatile	1	UDFN	64KB	FLASH EEPROM	Yes	Thermal monitoring	During Manufacturing by programming the image via firmware update process	N/A	By writing to Flash
 <b>NOTE:</b> For any information that you may need, direct your questions to your Dell Marketing contact.										

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