



Statement of Volatility – Dell Latitude 5520

⚠ CAUTION: A CAUTION indicates either potential damage to hardware or loss of data and tells you how to avoid the problem.

The Dell Latitude 5520 contains both volatile and non-volatile components. Volatile components lose their data immediately after power is removed from the component. Non-volatile components continue to retain their data even after power is removed from the component. The following Non-volatile components are present on the Latitude 5520 system board.

Table 1. List of Non-Volatile Components on System Board- For 10th Generation processors

Description	Reference Designator	Volatility Description	User Accessible for external data	Remedial Action (Action necessary to prevent loss of data)
SSD drive(s)	SSD1: M.2 2280	Non-Volatile magnetic media, various sizes in GB. SSD (solid state flash drive).	No	Low level format
System BIOS/EC	U2501 (32 MB)	Non-Volatile memory, Video BIOS for basic boot operation, PSA (on board diags), PXE diags.	No	N/A
Thunderbolt EEPROM	U7103	Non-Volatile memory, 8 Mbit (1 MB) (Thunderbolt FW)	No	N/A
LCD Panel EEDID EEPROM	Part of panel assembly	Non-Volatile memory, Stores panel manufacturing information, display configuration data	No	N/A
System Memory – DDR4 memory	Two DIMM on board DDR4 memory: DM1/DM2	Volatile memory in OFF state (see state definitions later in text)	Yes	Power off system
RTC CMOS	CPU1 (PCH)	Non-Volatile memory 256 bytes Stores CMOS information	No	N/A
Video memory – frame buffer	For UMA platform: Using system memory	Volatile memory in off state. UMA uses main system memory size allocated out of main memory.	No	Power off system
Intel ME Firmware	Combine on BIOS ROM	Non-Volatile memory, Intel ME firmware for system configuration, security and protection	No	N/A
Security Controller Serial Flash Memory	Combine on BIOS ROM	Non-Volatile memory	No	N/A
TPM Controller	U9101	Non-Volatile memory, 192K bits (24K bytes) ROM	No	N/A
ISH	Combine on BIOS ROM		No	N/A
Touch screen Embedded Flash	N/A	Non-Volatile memory	No	N/A
Digital IMVP9 controller	PU4601	Non-Volatile memory, 232 bits Digital IMVP8 controller	No	N/A

Description	Reference Designator	Volatility Description	User Accessible for external data	Remedial Action (Action necessary to prevent loss of data)
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△ CAUTION: All other components on the system board lose data if power is removed from the system. Primary power loss (unplugging the power cord and removing the battery) destroys all user data on the memory (DDR4, 3200 MHz). Secondary power loss (removing the on-board coin-cell battery) destroys system data on the system configuration and time-of-day information.

Table 2. List of Non-Volatile Components on System Board- For 11th Generation processors

Description	Reference Designator	Volatility Description	User Accessible for external data	Remedial Action (Action necessary to prevent loss of data)
SSD drive(s)	For UMA platform: SSD1: M.2 2280 SSD2: M.2 2280 For discrete platform: SSD1: M.2 2280	Non-Volatile magnetic media, various sizes in GB. SSD (solid state flash drive).	No	Low level format
System BIOS/EC	U2502 (32 MB)	Non-Volatile memory, Video BIOS for basic boot operation, PSA (on board diags), PXE diags.	No	N/A
Thunderbolt EEPROM	U7103	Non-Volatile memory, 8 Mbit (1 MB) (Thunderbolt FW)	No	N/A
LCD Panel EEDID EEPROM	Part of panel assembly	Non-Volatile memory, Stores panel manufacturing information, display configuration data	No	N/A
System Memory – DDR4 memory	Two DIMM on board DDR4 memory: DM1/DM2	Volatile memory in OFF state (see state definitions later in text)	Yes	Power off system
RTC CMOS	CPU1 (PCH)	Non-Volatile memory 256 bytes Stores CMOS information	No	N/A
Video memory – frame buffer	For UMA platform: Using system memory For discrete platform: NVIDIA N18S-G5	Volatile memory in off state. UMA uses main system memory size allocated out of main memory. DSC N18S-G5: frame buffer 80 GB/s	No	Power off system
Intel ME Firmware	Combine on BIOS ROM	Non-Volatile memory, Intel ME firmware for system configuration, security and protection	No	N/A
Security Controller Serial Flash Memory	Combine on BIOS ROM	Non-Volatile memory	No	N/A
TPM Controller	U9101	Non-Volatile memory, 192K bits (24K bytes) ROM	No	N/A
ISH	Combine on BIOS ROM		No	N/A

Description	Reference Designator	Volatility Description	User Accessible for external data	Remedial Action (Action necessary to prevent loss of data)
Touch screen Embedded Flash	N/A	Non-Volatile memory	No	N/A
Digital IMVP9 controller	PU4601	Non-Volatile memory, 232 bits Digital IMVP8 controller (Total 29 index, each index is 8 bits)	No	N/A

⚠ CAUTION: All other components on the system board lose data if power is removed from the system. Primary power loss (unplugging the power cord and removing the battery) destroys all user data on the memory (DDR4, 3200 MHz). Secondary power loss (removing the on-board coin-cell battery) destroys system data on the system configuration and time-of-day information.

In addition, to clarify memory volatility and data retention in situations where the system is put in different ACPI power states the following is provided (those ACPI power states are S0, S4, S5 and Modern Standby):

S0 state is the working state, where the dynamic RAM is maintained and is read/write by the processor.

Modern standby is a standby mode state that is different from S3 mode. In this state, the dynamic RAM is maintained.

S4 is called suspend to disk state or hibernate mode, with no power. In this state, the dynamic RAM is not maintained. If the system has been commanded to enter S4, the operating system writes the system context to a non-volatile storage file and leave appropriate context markers. When the system comes back to the working state, a restore file from the non-volatile storage can occur. The restore file must be valid. Dell systems will be able to go to S4 if the operating system and the peripherals support S4 state. Windows 7/8 support S4 state.

S5 is the soft off state, with no power. The operating system does not save any context to wake up the system. No data will remain in any component on the system board, that is cache or memory. The system requires a complete boot when awakened. Since S5 is the shut off state, coming out of S5 requires power on which clears all registers.

The following table shows all the states supported by Dell Latitude 5520:

Model Number	S0	Modern standby	S4	S5
Dell Latitude 5520	v	v	v	v