Statement of Volatility - Dell 1708FPf, 1908FPf Monitor

The purpose of this document is to certify that Dell’s XXXX monitor will not save, retain or reproduce a signal to any internal or external component after power has been removed and reapplied to the unit.

The Dell XXXX monitor contains both volatile and non-volatile (NV) memory ICs. Volatile memory(s) lose their data immediately upon removal of power. Non-volatile memory ICs continue to retain their data even after the power has been removed. However, no input video data is written into these memory ICs during operation.

List below contains volatile and non volatile memory ICs used in Dell XXXX monitor.

<table>
<thead>
<tr>
<th>Flash ROM</th>
<th>NA</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>16 Mbit</td>
</tr>
<tr>
<td>Type [e.g. Flash PROM, EEPROM]</td>
<td>Serial flash memory</td>
</tr>
<tr>
<td>Volatility</td>
<td>Non-volatile</td>
</tr>
<tr>
<td>Can user programs or operating system write data to it during normal operation?</td>
<td>No</td>
</tr>
<tr>
<td>Purpose</td>
<td>To store firmware</td>
</tr>
<tr>
<td>How is data input to this memory?</td>
<td>Loading flash memory requires a vendor-provided firmware file</td>
</tr>
<tr>
<td>How is this memory write protected?</td>
<td>Software write protected</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>DDR SDRAM</th>
<th>NA</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>8x16 Mbit</td>
</tr>
<tr>
<td>Type [e.g. Flash PROM, EEPROM]</td>
<td>DDR SDRAM</td>
</tr>
<tr>
<td>Volatility</td>
<td>Volatile</td>
</tr>
<tr>
<td>Can user programs or operating system write data to it during normal operation?</td>
<td>No</td>
</tr>
<tr>
<td>Purpose</td>
<td>Buffer of video decoder</td>
</tr>
<tr>
<td>How is data input to this memory?</td>
<td>Video is received by video decoder chip(CVBS processor) and processed in SDRAM.</td>
</tr>
<tr>
<td>How is this memory write protected?</td>
<td>No write protect mechanism</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>DDR2 SDRAM</th>
<th>NA</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>32x16 Mbit</td>
</tr>
<tr>
<td>Type [e.g. Flash PROM, EEPROM]</td>
<td>DDR2 SDRAM</td>
</tr>
<tr>
<td>-------------------------------</td>
<td>------------</td>
</tr>
<tr>
<td>Volatility</td>
<td>Volatile</td>
</tr>
<tr>
<td>Can user programs or operating system write data to it during normal operation?</td>
<td>No</td>
</tr>
<tr>
<td>Purpose</td>
<td>Buffer for video processing</td>
</tr>
<tr>
<td>How is data input to this memory?</td>
<td>Video is received by monitor core chip and processed in SDRAM.</td>
</tr>
<tr>
<td>How is this memory write protected?</td>
<td>No write protect mechanism</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>System EEPROM</th>
<th>AT24C04N</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>4Kbit</td>
</tr>
<tr>
<td>Type [e.g. Flash PROM, EEPROM]</td>
<td>EEPROM</td>
</tr>
<tr>
<td>Volatility</td>
<td>Non-volatile</td>
</tr>
<tr>
<td>Can user programs or operating system write data to it during normal operation?</td>
<td>Yes</td>
</tr>
<tr>
<td>Purpose</td>
<td>Storage of system setting(OSD)</td>
</tr>
<tr>
<td>How is data input to this memory?</td>
<td>Control the OSD menu and change OSD setting(ex. Brightness,contrast,color setting) and the setting will be stored into system EEPROM</td>
</tr>
<tr>
<td>How is this memory write protected?</td>
<td>Software write protected</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>VGA EDID EEPROM</th>
<th>AT24C02AN</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>2Kbit</td>
</tr>
<tr>
<td>Type [e.g. Flash PROM, EEPROM]</td>
<td>EEPROM</td>
</tr>
<tr>
<td>Volatility</td>
<td>Non-volatile</td>
</tr>
<tr>
<td>Can user programs or operating system write data to it during normal operation?</td>
<td>No</td>
</tr>
<tr>
<td>Purpose</td>
<td>To store VGA EDID</td>
</tr>
<tr>
<td>How is data input to this memory?</td>
<td>Writing EDID requires a vendor-provided tool and an specific VGA cable.</td>
</tr>
<tr>
<td>How is this memory write protected?</td>
<td>Hardware and software write protected</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>DVI EDID EEPROM</th>
<th>AT24C02AN</th>
</tr>
</thead>
<tbody>
<tr>
<td>Size</td>
<td>2Kbit</td>
</tr>
<tr>
<td>Type [e.g. Flash PROM, EEPROM]</td>
<td>EEPROM</td>
</tr>
<tr>
<td>-------------------------------</td>
<td>---------</td>
</tr>
<tr>
<td>Volatility</td>
<td>Non-volatile</td>
</tr>
<tr>
<td>Can user programs or operating system write data to it during normal operation?</td>
<td>No</td>
</tr>
<tr>
<td>Purpose</td>
<td>To store DVI EDID</td>
</tr>
<tr>
<td>How is data input to this memory?</td>
<td>Writing EDID requires a vendor-provided tool and an specific DVI cable.</td>
</tr>
<tr>
<td>How is this memory write protected?</td>
<td>Hardware write protected</td>
</tr>
<tr>
<td><strong>Flash ROM</strong></td>
<td>SST25LF020A</td>
</tr>
<tr>
<td>Size</td>
<td>2M</td>
</tr>
<tr>
<td>Type [e.g. Flash PROM, EEPROM]</td>
<td>Serial flash memory</td>
</tr>
<tr>
<td>Volatility</td>
<td>Non-volatile</td>
</tr>
<tr>
<td>Can user programs or operating system write data to it during normal operation?</td>
<td>No</td>
</tr>
<tr>
<td>Purpose</td>
<td>To store firmware</td>
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<tr>
<td>How is data input to this memory?</td>
<td>Loading flash memory requires a vendor-provided firmware</td>
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<td>How is this memory write protected?</td>
<td>Hardware write protected</td>
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